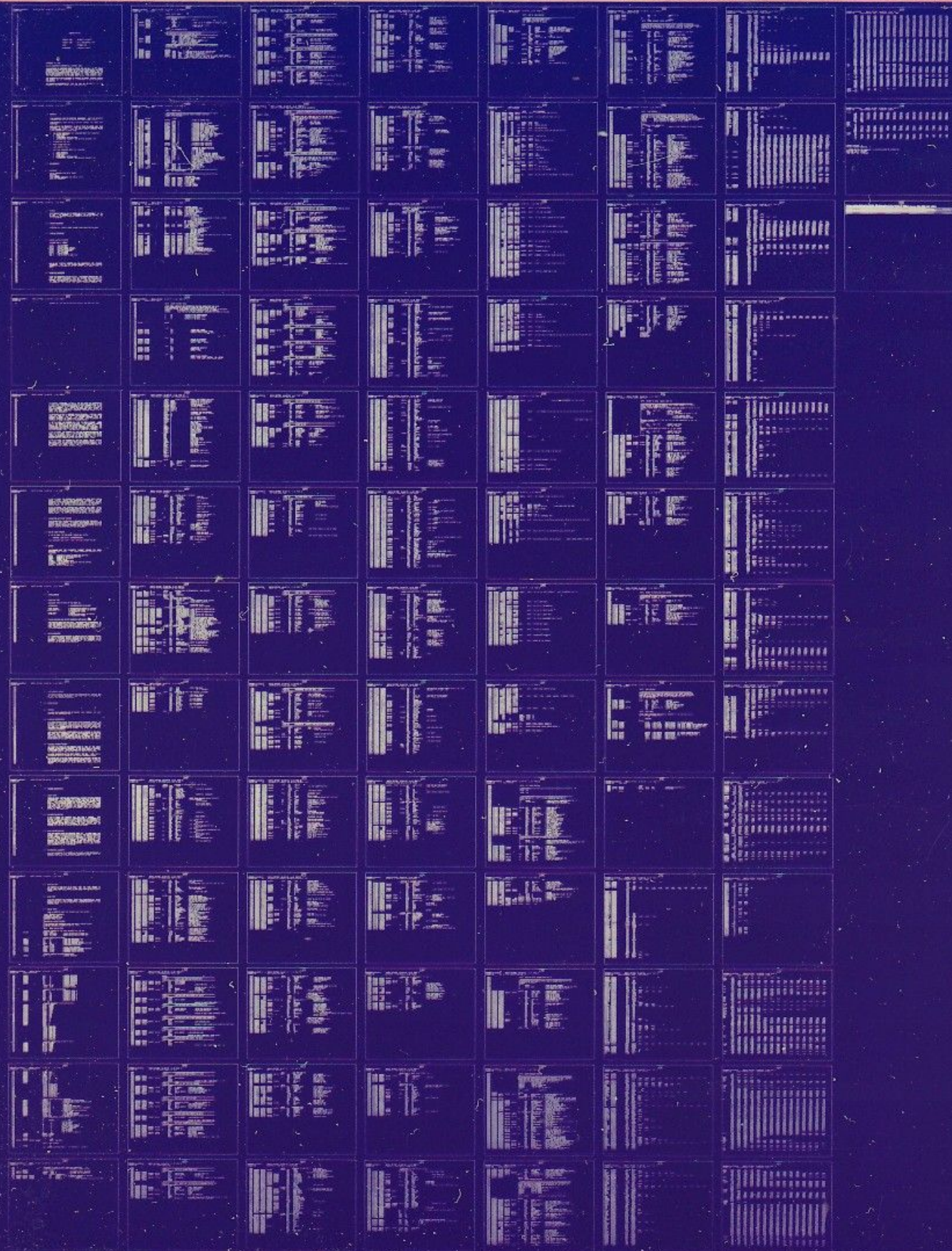


ADV11

PERFORMANCE TEST
MD-11-DVADA-A

EP-DVADA-A-DL-A
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MAINDEC-11-DVADA-A
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MACY11 27(732) 21-OCT-76 11:18 PAGE 2
801
PRODUCT CODE: MAINDEC-11-DVADA-A
PRODUCT NAME: ADV11 PERFORMANCE TEST
DATE: OCTOBER 1976
MAINTAINER: DIAGNOSTIC GROUP
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IDENTIFICATION

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1.0 ABSTRACT

THIS DIAGNOSTIC HAS TWO STARTING ADDRESSES: 200 FOR STANDARD TOLERANCES AND 210 FOR THE OPTION TEST AREA'S BURN IN TEST.

THIS DIAGNOSTIC TESTS THE ADV11 WITH OR WITHOUT THE BERG TEST CONNECTOR.

WHEN STARTING THE DIAGNOSTIC, A SET OF TESTS IS LISTED AND THIS STATEMENT IS PRINTED OUT: "TYPE THE LETTER AND CARRIAGE RETURN OF THE DESIRED TEST:". THE FOLLOWING CHART INDICATES WHICH LETTER CORRESPONDS TO WHICH TEST:

- M: THE ENTIRE WRAPAROUND TEST (REQUIRES BERG TEST CONNECTOR)
 - A. ANALOG SUBTESTS
 - B. NOISE TEST
 - C. INTERCHANNEL SETTLING TEST
 - D. DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST
- C: CALIBRATION TEST ONLY
- P: PRINT VALUES TEST ONLY
- L: LOGIC SUBTESTS ONLY
- A: AUTO TEST (REQUIRES BERG TEST CONNECTOR)
 - A. LOGIC SUBTESTS
 - B. ANALOG SUBTESTS
 - C. NOISE TEST
 - D. INTERCHANNEL SETTLING TEST
 - E. DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST

2.0 REQUIREMENTS

2.1 EQUIPMENT

- LSI-11 COMPUTER WITH 8K OF MEMORY
- TELETYPE
- ADV11 MODULE
- VT55 TERMINAL SUPPORTED FOR GRAPHIC OUTPUT
- BERG TEST CONNECTOR

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2.2 STORAGE

THIS PROGRAM USES ALL BK OF MEMORY AND IS NOT "CHAINABLE" ON AN BK CPU. THE PROGRAM IS "CHAINABLE" ON 12K OR GREATER. THE PROGRAM WILL DESTROY "ABSOLUTE LOADER" ON AN BK CPU, IF "W" OR "A" IS SELECTED.

3.0 LOADING PROCEDURE

PROCEDURE FOR LOADING NORMAL BINARY TAPES SHOULD BE FOLLOWED.

4.0 STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

STANDARD PDP-11 FORMAT

SW15=1 HALT ON ERROR
SW14=1 LOOP ON TEST
SW13=1 INHIBIT ERROR TYPEOUTS
SW12=1 HALT FOR VTSS DISPLAY
SW11=1 INHIBIT ITERATIONS
SW10=1 BELL ON ERROR
SW9 =1 LOOP ON ERROR
SW8 =1 LOOP ON TEST IN SWR <7:0>

200 IS THE STARTING ADDRESS OF THE DIAGNOSTIC FOR STANDARD TOLERANCES. 204 IS THE RESTART ADDRESS. 210 IS THE STARTING ADDRESS OF THE DIAGNOSTIC FOR THE OPTION TEST AREA'S BURN IN TEST.

5.0 OPERATING PROCEDURE

START THE DIAGNOSTIC AT 200 OR 210. THE PROGRAM HEADING AND THE LIST OF TESTS AVAILABLE, WILL BE PRINTED OUT FOLLOWED BY A MESSAGE "TYPE LETTER AND <CR> FOR TEST:". THEN TYPE THE LETTER YOU WANT, ACCORDING TO THE TABLE LISTED AND HIT CARRIAGE RETURN. IF STARTED AT THE OPTION TEST AREA'S STARTING ADDRESS, THE

MAINDEC-11-DVADA-A
DVADAA.CMB

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EO1

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PROGRAM WILL NOT ASK FOR THE TEST BUT WILL RUN THE LOGIC TEST.

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TWO CONTROL CHARACTERS, 1A AND 1C, ARE SET ASIDE FOR INTERRUPTING A TEST AND TRANSFERRING CONTROL TO EITHER THE BEGINNING OF THE DIAGNOSTIC (1C) OR TO THE BEGINNING OF THE SPECIFIC TEST WHICH WAS IN PROGRESS (1A). DURING THE LOGIC TESTS WHILE A RESET IS BEING PERFORMED, 1C OR 1A WILL NOT BE EXECUTED UNTIL AFTER THE RESET HAS BEEN COMPLETED, THEREFORE CONTINUE TYPING 1C OR 1A UNTIL IT IS SUCCESSFUL.

FOR MACHINES WITHOUT A HARDWARE SWITCH REGISTER, LOCATION SWREG (176) IS USED AS A SOFTWARE SWITCH REGISTER. TO MODIFY THE CONTENTS OF SWREG, TYPE 1G. THE PROGRAM RESPONDS WITH THE CURRENT CONTENTS OF SWREG AND A SLASH. TYPE THE DESIRED NEW CONTENTS OF SWREG FOLLOWED BY A CARRIAGE RETURN.

IF "W" IS TYPED, THE PROGRAM WILL TYPE "XX ADV11'S FOUND". WHERE XX IS THE NUMBER OF ADV11'S IN OCTAL. IF THE NUMBER IS GREATER THAN 1, THE TEST WILL BE RUN SUCCESSIVELY ON EACH ADV11. THE PROGRAM WILL RUN THROUGH THE ANALOG SUBTESTS, THE NOISE TEST, THE INTERCHANNEL SETTLING TEST, AND THE DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST. THE BERG TEST CONNECTOR IS REQUIRED.

IF "C" IS TYPED, THE PROGRAM WILL RUN THE CALIBRATION ROUTINE AND LOOP ON THE TEST UNTIL IT IS CALIBRATED AND A CARRIAGE RETURN TYPED. IF A CERTAIN ADV11 IS TO BE CALIBRATED, ITS STATUS REGISTER ADDRESS MUST BE LOADED INTO \$BASE (1250), AND ITS VECTOR ADDRESS MUST BE LOADED INTO THE LOW BYTE OF \$VECT1 (1244).

IF "P" IS TYPED, THE PROGRAM WILL RUN THE PRINT VALUES ROUTINE AND WILL LOOP ON THAT TEST UNTIL THE OPERATOR HALTS IT. IF A CERTAIN ADV11 IS TO BE TESTED, ITS STATUS REGISTER ADDRESS MUST BE LOADED INTO \$BASE (1250), AND ITS VECTOR ADDRESS MUST BE LOADED INTO THE LOW BYTE OF \$VECT1 (1244).

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IF "A" IS TYPED, THE PROGRAM WILL EXECUTE THE LOGIC TESTS, ANALOG TESTS, NOISE, SETTLE AND DIFFERENTIAL LINEARITY. AT THE BEGINNING OF THE TEST THE PROGRAM WILL TYPE "XX ADV11'S FOUND". WHERE XX IS THE NUMBER OF ADV11'S IN OCTAL. IF THE NUMBER IS GREATER THAN 1 THE TEST WILL RUN SUCCESSIVELY ON EACH ADV11.

IF "L" IS TYPED, THE PROGRAM WILL EXECUTE THE LOGIC TESTS, PRINTING "END PASS" WHEN IT HAS COMPLETED AN ENTIRE PASS. AT THE BEGINNING OF THE TEST THE PROGRAM WILL TYPE "XX ADV11'S FOUND". WHERE XX IS THE NUMBER OF ADV11'S IN OCTAL. IF THE NUMBER IS GREATER THAN 1, THE TEST WILL BE RUN SUCCESSIVELY ON EACH ADV11.

5.1 INHIBITING AUTO-SIZE FEATURE

THIS PROGRAM WILL AUTOMATICALLY AUTO-SIZE AND TEST EACH ADV11 IT DETECTS ON THE SYSTEM. TO INHIBIT THIS FEATURE, SET BIT 15 OF LOCATION SEMVM (1214). ALSO, LOAD LOCATION SBASE (1250) WITH THE ADV11'S STATUS REGISTER ADDRESS AND THE LOW BYTE OF LOCATION SVECT1 (1244) WITH THE ADV11'S VECTOR ADDRESS.

5.2 END OF PASS TIMEOUTS

AT END OF PASS, THE FOLLOWING TIMEOUT WILL OCCUR:

"ENDPASS GOOD UNITS 000000000000011

THIS INDICATES THAT UNITS 1 AND 2 HAVE RUN WITHOUT FAILURE.

6.0 ERRORS

THIS PROGRAM USES THE DIAGNOSTIC "SYSMAC" PACKAGE FOR ERROR REPORTING AND TIMEOUT. THE ERROR INFORMATION CONSISTS OF THE FOLLOWING:

- ERRPC: LOCATION AT WHICH AN ERROR WAS DETECTED.
- STREG: ADDRESS OF THE STATUS REGISTER.
- ADBUFF: ADDRESS OF THE BUFFER
- CHANL: CHANNEL VALUE
- NOMINAL: EXPECTED CORRECT DATA
- TOLERANCE: THE ACCEPTABLE DEVIATION FROM THE NOMINAL
- ACTUAL: ACTUAL DATA
- EXPECTED: EXPECTED CORRECT DATA

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7.0 MISCELLANEOUS

7.1 EXECUTION TIME

EXECUTION TIME FOR EACH OF THE TESTS IS:

CALIBRATION:	5 CONVERSIONS/MIN @110 BAUD
PRINT VALUES:	8 CONVERSIONS/8 SECONDS @ 110 BAUD
WRAPAROUND TEST:	7 MINUTES FIRST PASS; 32 MINUTES FOR SUCCESSIVE PASSES
LOGIC TEST:	1 MINUTE
AUTO TEST:	8 MINUTES FIRST PASS, 33 MINUTES FOR SUCCESSIVE PASSES

7.2 STATUS REGISTER AND VECTOR ADDRESSES AND PRIORITY

WHEN TESTING MORE THAN ONE ADV11, THE DIFFERENCE IN ADDRESSES IS 4 FOR BUS ADDRESS AND 10 FOR VECTOR ADDRESS. THESE VALUES ARE IN VADR (BUS ADDRESS) (1332) AND VVCT (VECTOR ADDRESS) (1334). THE FIRST ADV11'S STATUS REGISTER ADDRESS MUST BE IN \$BASE (1250), ITS VECTOR ADDRESS MUST BE IN THE LOW BYTE OF \$VECT1 (1244).

7.3 SWITCH REGISTER

IF A HARDWARE SWITCH REGISTER IS PRESENT AND THE OPERATOR DESIRES TO USE A SOFTWARE SWITCH REGISTER AND THE ?G FEATURE; IT IS NECESSARY TO LOAD THE STARTING ADDRESS, SET THE HARDWARE SWITCH REGISTER TO ALL ONES (-1), AND HIT START. THE PROGRAM WILL THEN RUN WITH THE SOFTWARE SWITCH REGISTER.

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7.4 VTSS GRAPHIC OUTPUT

THE SCREEN DISPLAY MAY BE HALTED FOR EXAMINATION BY SETTING BIT 12 OF THE SWITCH REGISTER. AND THEN JUST HIT CONTINUE TO COMPLETE THE PROGRAM'S EXECUTION.

8.0 RESTRICTIONS

8.1 TESTING

THE BERG TEST CONNECTOR MUST BE PRESENT WHEN RUNNING THE AUTO TEST AND THE WRAPAROUND TEST.

8.2 STARTING RESTRICTION

IF A FREE-RUNNING CLOCK, SUCH AS 60HZ FROM THE POWER SUPPLY, IS ATTACHED TO THE BEVNT BUS LINE ON BOTH REV LEVEL C/D AND E SYSTEMS, AN INTERRUPT TO LOCATION 100 WILL OCCUR WHEN USING THE "G" AND "L" COMMANDS PRIOR TO EXECUTING THE FIRST INSTRUCTION. THEREFORE THIS PROGRAM CAN NOT DISABLE THE BEVNT BUS LINE BY INHIBITING INTERRUPTS.

USER SYSTEMS REQUIRING A FREE-RUNNING CLOCK ATTACHED TO THE BEVNT BUS LINE CAN TEMPORARILY AVOID THIS SITUATION BY SETTING THE PSW(R5) TO 200, INSTEAD OF USING THE "G" COMMAND. LOAD THE PC (R7) WITH THE STARTING ADDRESS AND USE THE PROCEED "P" COMMAND. BEFORE USING THE "L" COMMAND, THE PSW(R5) CAN BE SET TO 200 TO AVOID RECEIVING THE BEVNT INTERRUPT AFTER LOADING THE ABS LOADER.

8.3 POSSIBLE PROGRAM "BOMBS"

THE FIRST TWO TESTS OF THIS PROGRAM CHECK TO SEE IF THE ADV11 RESPONDS TO THE EXPECTED ADDRESS. IF THE ADV11 DOES NOT RESPOND, A BUSS ERROR OCCURS. ALSO BUS ERRORS CAN OCCUR DURING THE TIME THE PROGRAM SIZES TO SEE HOW MANY ADV11'S ARE ON YOUR SYSTEM.

FOR MORE INFORMATION ON THE NEXT SUBJECT, SEE JAN. 1976 LSI-11 ENGINEERING BULLETIN ISSUED BY THE DIGITAL COMPONENTS GROUP.

BUS ERRORS MAY ALTER THE PRESET CONTENTS OF LOCATION 4 BEFORE THE TRAP IS EXECUTED, THEREBY TRANSFERRING PROGRAM CONTROL TO AREA IN THE PROGRAM THAT WAS NOT SET UP TO HANDLE THE TRAP. IF THIS HAPPENS, THE PROGRAM WILL "BOMB" AND POSSIBLY REWRITE PARTS OF ITSELF.

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9.0 PROGRAM DESCRIPTION

9.1 LOGIC TESTS

THESE 23 LOGIC SUBTESTS RUN SEQUENTIALLY WITHOUT FURTHER OPERATOR INTERVENTION. ITS PURPOSE IS TO CHECK THAT EACH OF THE STATUS REGISTER BITS THAT ARE READ/WRITE CAN BE LOADED AND PROPERLY READ BACK; THAT INITIALIZE CLEARS THE EXTERNAL START ENABLE BIT, THE DONE BIT, THE INTERRUPT ENABLE BIT, THE OVERFLOW BIT, THE ERROR FLAG, AND THE A/D START BIT. IT ALSO CHECKS THAT THE A/D DONE FLAG SETS AT END OF CONVERSION AND CLEARS WHEN THE CONVERTED VALUE IS READ. IT CHECKS THE INTERRUPT LOGIC AND THE CORRECT SETTING OF THE ERROR FLAG.

9.2 CALIBRATION ROUTINE

IF "C" IS TYPED, THE PROGRAM WILL ASK FOR A CHANNEL. TYPE CHANNEL NUMBER FOLLOWED BY A CARRIAGE RETURN. THE PROGRAM WILL ASK YOU IF YOU WANT OFFSET OR GAIN. APPLY VOLTAGE REQUESTED TO SELECTED CHANNEL. ADJUST POT REQUESTED FOR 0.00 LSB TYPEOUT. TYPE CARRIAGE RETURN WHEN ADJUSTED. THE LAST TYPEOUT WILL BE CHECKED FOR 0.00 LSB WITH A TOLERANCE OF 0.04 LSB IF OUTSIDE, THE PROGRAM WILL ASK YOU TO ADJUST THE SAME POT AGAIN.

9.3 PRINT VALUES ROUTINE

THIS TEST BEGINS WHEN THE OPERATOR TYPES "P". IT THEN LOADS THE CHANNEL FROM THE SWITCH REGISTER BITS 0-7 AND DOES A CONVERSION ON THAT CHANNEL. IF SWR BIT 13 IS DOWN (0), IT PRINTS OUT THE CONVERTED VALUE ON THE TELETYPE; OTHERWISE, IF SWR BIT 13 IS UP (1), IT PUTS THE CONVERTED VALUE IN THE DISPLAY REGISTER. THE OPERATOR MAY CHANGE THE CHANNEL AT ANY TIME DURING THE TEST. HOWEVER THE NEW VALUES FROM THE NEW CHANNEL WILL NOT BE PRINTED UNTIL THE NEXT LINE OF 8 VALUES IS PRINTED. THE 8 VALUES ON EACH LINE CORRESPOND TO ONLY ONE CHANNEL.

9.4 DIFFERENTIAL LINEARITY

THIS TEST DETERMINE IF A CHANGE IN THE INPUT VOLTAGE REPRESENTS A SIMILAR CHANGE IN THE RESULTING CONVERTED BINARY VALUE, BY MEASURING THE WIDTH OF EACH STATE CORRECT TO 0.01 LSB.

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9.5 SETTLING TEST

THE PURPOSE OF THIS TEST IS TO CHECK THAT THE TIME NEEDED TO SETTLE AND CORRECTLY REPORT A NEW INPUT VALUE AFTER SWITCHING CHANNELS DOES NOT EXCEED THE EXPECTED AMOUNT OF TIME FOR SUCH A CHANGE.

9.6 NOISE TEST

THIS TEST MEASURES THE INTERNAL SHORT-TERM REPEATABILITY NOISE WITHIN THE A/D. RMS NOISE EQUALS 1 STANDARD DEVIATION OF THE GAUSSIAN CURVE, PEAK NOISE EQUALS 2.3 STANDARD DEVIATION OF THE GAUSSIAN CURVE.

9.7 ANALOG TESTS

THESE 6 SUBTESTS CHECK THE CHANNELS AND THEIR OUTPUT.

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*MAYNARD, MASS. 01754
*
*PROGRAM BY GEORGE STEVENS
*
*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
*PACKAGE (MAINDEC-11-DZQAC-CO), MAR 21, 1976.
*
*SBTTL BASIC DEFINITIONS

001100

.*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***

STACK= 1100

.EQUIV EMT,ERROR ;;BASIC DEFINITION OF ERROR CALL

.EQUIV IOT,SCOPE ;;BASIC DEFINITION OF SCOPE CALL

.*MISCELLANEOUS DEFINITIONS

HT= 11 ;;CODE FOR HORIZONTAL TAB

LF= 12 ;;CODE FOR LINE FEED

CR= 15 ;;CODE FOR CARRIAGE RETURN

CRLF= 200 ;;CODE FOR CARRIAGE RETURN-LINE FEED

PS= 177776 ;;PROCESSOR STATUS WORD

.EQUIV PS,PSW

STKLMT= 177774 ;;STACK LIMIT REGISTER

PIRQ= 177772 ;;PROGRAM INTERRUPT REQUEST REGISTER

DSWR= 177570 ;;HARDWARE J1TCH REGISTER

DOISP= 177570 ;;HARDWARE DISPLAY REGISTER

.*GENERAL PURPOSE REGISTER DEFINITIONS

RO= %D ;;GENERAL REGISTER

000011

000012

000015

000200

177776

177774

177772

177570

177570

000000

452	000001	R1=	%1	:: GENERAL REGISTER
453	000002	R2=	%2	:: GENERAL REGISTER
454	000003	R3=	%3	:: GENERAL REGISTER
455	000004	R4=	%4	:: GENERAL REGISTER
456	000005	R5=	%5	:: GENERAL REGISTER
457	000006	R6=	%6	:: GENERAL REGISTER
458	000007	R7=	%7	:: GENERAL REGISTER
459		.EQUIV	R6,SP	:: STACK POINTER
460		.EQUIV	R7,PC	:: PROGRAM COUNTER

.*PRIORITY LEVEL DEFINITIONS

461		PRO=	0	:: PRIORITY LEVEL 0
462	000000	PRI=	40	:: PRIORITY LEVEL 1
463	000040	PR2=	100	:: PRIORITY LEVEL 2
464	000100	PR3=	140	:: PRIORITY LEVEL 3
465	000140	PR4=	200	:: PRIORITY LEVEL 4
466	000200	PR5=	240	:: PRIORITY LEVEL 5
467	000240	PR6=	300	:: PRIORITY LEVEL 6
468	000300	PR7=	340	:: PRIORITY LEVEL 7
469	000340			

.*"SWITCH REGISTER" SWITCH DEFINITIONS

470		SW15=	100000	
471	100000	SW14=	40000	
472	040000	SW13=	20000	
473	020000	SW12=	10000	
474	010000	SW11=	4000	
475	004000	SW10=	2000	
476	002000	SW09=	1000	
477	001000	SW08=	400	
478	000400	SW07=	200	
479	000200	SW06=	100	
480	000100	SW05=	40	
481	000040	SW04=	20	
482	000020	SW03=	10	
483	000010	SW02=	4	
484	000004	SW01=	2	
485	000002	SW00=	1	
486	000001	.EQUIV	SW09,SW9	
487		.EQUIV	SW08,SW8	
488		.EQUIV	SW07,SW7	
489		.EQUIV	SW06,SW6	
490		.EQUIV	SW05,SW5	
491		.EQUIV	SW04,SW4	
492		.EQUIV	SW03,SW3	
493		.EQUIV	SW02,SW2	
494		.EQUIV	SW01,SW1	
495		.EQUIV	SW00,SW0	

.*DATA BIT DEFINITIONS (BIT00 TO BIT15)

496	100000	BIT15=	100000	
497	040000	BIT14=	40000	
498	020000	BIT13=	20000	
499	010000	BIT12=	10000	
500	004000	BIT11=	4000	
501	002000	BIT10=	2000	
502	001000	BIT09=	1000	

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400
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000100
000040
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000004
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000001

BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1
.EQUIV BIT09,BIT9
.EQUIV BIT08,BIT8
.EQUIV BIT07,BIT7
.EQUIV BIT06,BIT6
.EQUIV BIT05,BIT5
.EQUIV BIT04,BIT4
.EQUIV BIT03,BIT3
.EQUIV BIT02,BIT2
.EQUIV BIT01,BIT1
.EQUIV BIT00,BIT0

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000010
000014
000014
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.*BASIC "CPU" TRAP VECTOR ADDRESSES
ERRVEC= 4 : TIME OUT AND OTHER ERRORS
RESVEC= 10 : RESERVED AND ILLEGAL INSTRUCTIONS
TBITVEC=14 : "T" BIT
TRTVEC= 14 : TRACE TRAP
BPTVEC= 14 : BREAKPOINT TRAP (BPT)
IOTVEC= 20 : INPUT/OUTPUT TRAP (IOT) **SCOPE**
PWRVEC= 24 : POWER FAIL
EMTVEC= 30 : EMULATOR TRAP (EMT) **ERROR**
TRAPVEC=34 : "TRAP" TRAP
TKVEC= 60 : TTY KEYBOARD VECTOR
TPVEC= 64 : TTY PRINTER VECTOR
PIRQVEC=240 : PROGRAM INTERRUPT REQUEST VECTOR

.SBTTL OPERATIONAL SWITCH SETTINGS
*
* SWITCH USE
*-----
* 15 HALT ON ERROR
* 14 LOOP ON TEST
* 13 INHIBIT ERROR TYPEOUTS
* 12 H'LT FOR VT55 DISPLAY
* 11 INHIBIT ITERATIONS
* 10 BELL ON ERROR
* 9 LOOP ON ERROR
* 8 LOOP ON TEST IN SWR<7:0>

ABASE= 170400
AVECT1= 100400
APRIOR= 200

000100 000104 000200 000002

.=100
.WORD 104,200,2

.SBTTL TRAP CATCHER

000000

.=0
;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"

```

564 ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
565 ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
566 ;=174
567 000174 000000 DISPREG: .WORD 0 ;; SOFTWARE DISPLAY REGISTER
568 000176 000000 SWREG: .WORD 0 ;; SOFTWARE SWITCH REGISTER
569 .SBTTL STARTING ADDRESS(ES)
570 000200 000137 001644 JMP @#BEGIN ;; JUMP TO STARTING ADDRESS OF PROGRAM
571 000204 000137 002262 JMP @#BEG2 ; RESTART ADDRESS
572 000210 000137 001652 JMP @#BEGIN2 ; START ADDRESS FOR OPTION TEST AREA

```

```

573          .SBTTL ACT11 HOOKS
574
575          ;;*****
576          ;HOOKS REQUIRED BY ACT11
577          $SVPC=.          ;SAVE PC
578          .=46
579          SENDAD          ;;1)SET LOC.46 TO ADDRESS OF SENDAD IN .SEOP
580          .=52
581          .WORD 0          ;;2)SET LOC.52 TO ZERO
582          .=52
583          .=52          ;; RESTORE PC
584          .=1000
585          .SBTTL APT PARAMETER BLOCK
586
587          ;;*****
588          ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
589          ;;*****
590          .SX=.          ;SAVE CURRENT LOCATION
591          .=24          ;SET POWER FAIL TO POINT TO START OF PROGRAM
592          200          ;FOR APT START UP
593          .=44          ;POINT TO APT INDIRECT ADDRESS PNTR.
594          $APTHDR          ;POINT TO APT HEADER BLOCK
595          .=.SX          ;RESET LOCATION COUNTER
596          ;;*****
597          ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
598          ;INTERFACE SPEC.
599
600          $APTHD:
601          $SHIBTS: .WORD 0          ;; TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
602          $SEADR: .WORD $MAIL          ;; ADDRESS OF APT MAILBOX (BITS 0-15)
603          $STLTM: .WORD 1200.          ;; RUN TIM OF LONGEST TEST
604          $P45TM: .WORD 500.          ;; RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
605          $LNTM: .WORD 1700.          ;; ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
          SETEND=$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)

```

606
607
608
609
610
611
612 001100
613 00:100
614 001100 000000
615 001102 000
616 001103 000
617 001104 000000
618 001106 000000
619 001110 000000
620 001112 000000
621 001114 000
622 001115 001
623 001116 000000
624 001120 000000
625 001122 000000
626 001124 000000
627 001126 000000
628 001128 000000
629 001130 000000
630 001132 000000
631 001134 000
632 001136 000
633 001138 000000
634 001140 177570
635 001142 177570
636 001144 177560
637 001146 177562
638 001148 177564
639 001150 177566
640 001152 000
641 001154 002
642 001156 012
643 001158 000
644 001160 000000
645 001162 000000
646 001164 177607 000377
647 001170 077
648 001171 015
649 001172 000012
650
651 001174
652 001174 000000
653 001176 000000
654 001200 000000
655 001202 000000
656 001204 000000
657 001206 000000
658 001210 000000

.SBTTL COMMON TAGS

: THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
: USED IN THE PROGRAM.

SCHTAG: . =1100 ;: START OF COMMON TAGS
STSTNM: .WORD 0 ;: CONTAINS THE TEST NUMBER
SERFLG: .BYTE 000 ;: CONTAINS ERROR FLAG
\$ICH: .WORD 000 ;: CONTAINS SUBTEST ITERATION COUNT
\$LPADR: .WORD 000 ;: CONTAINS SCOPE LOOP ADDRESS
\$LPERR: .WORD 000 ;: CONTAINS SCOPE RETURN FOR ERRORS
\$ERTTL: .WORD 000 ;: CONTAINS TOTAL ERRORS DETECTED
\$ITEMB: .BYTE 001 ;: CONTAINS ITEM CONTROL BYTE
\$ERMAX: .BYTE 1 ;: CONTAINS MAX. ERRORS PER TEST
\$ERRPC: .WORD 000 ;: CONTAINS PC OF LAST ERROR INSTRUCTION
\$GADR: .WORD 000 ;: CONTAINS ADDRESS OF 'GOOD' DATA
\$BADADR: .WORD 000 ;: CONTAINS ADDRESS OF 'BAD' DATA
\$GOODAT: .WORD 000 ;: CONTAINS 'GOOD' DATA
\$BADAT: .WORD 000 ;: CONTAINS 'BAD' DATA
 ;: RESERVED--NOT TO BE USED
\$AUTOB: .BYTE 0 ;: AUTOMATIC MODE INDICATOR
\$INTAG: .BYTE 0 ;: INTERRUPT MODE INDICATOR
\$SWR: .WORD 0SWR ;: ADDRESS OF SWITCH REGISTER
\$DISP: .WORD 0DISP ;: ADDRESS OF DISPLAY REGISTER
\$TKS: 177560 ;: TTY KBD STATUS
\$TKB: 177562 ;: TTY KBD BUFFER
\$TPS: 177564 ;: TTY PRINTER STATUS REG. ADDRESS
\$TPB: 177566 ;: TTY PRINTER BUFFER REG. ADDRESS
\$NULL: .BYTE 0 ;: CONTAINS NULL CHARACTER FOR FILLS
\$FILLS: .BYTE 2 ;: CONTAINS # OF FILLER CHARACTERS REQUIRED
\$FILLC: .BYTE 12 ;: INSERT FILL CHARS. AFTER A "LINE FEED"
\$TPFLG: .BYTE 0 ;: "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
\$TIMES: 0 ;: MAX. NUMBER OF ITERATIONS
\$ESCAPE: 0 ;: ESCAPE ON ERROR ADDRESS
\$BELL: .ASCIZ <207><377><377> ;: CODE FOR BELL
\$QUES: .ASCII '?' ;: QUESTION MARK
\$CRLF: .ASCII <15> ;: CARRIAGE RETURN
\$LF: .ASCIZ <12> ;: LINE FEED

.SBTTL APT MAILBOX-ETABLE

: EVEN
\$MAIL: .WORD ;: APT MAILBOX
\$MSGTY: .WORD AMSGTY ;: MESSAGE TYPE CODE
\$FATAL: .WORD AFATAL ;: FATAL ERROR NUMBER
\$TESTN: .WORD ATESTN ;: TEST NUMBER
\$PASS: .WORD APASS ;: PASS COUNT
\$DEVCT: .WORD ADEVCT ;: DEVICE COUNT
\$UNIT: .WORD AUNIT ;: I/O UNIT NUMBER
\$MSGAD: .WORD AMSGAD ;: MESSAGE ADDRESS

662	001212	000000	\$MSGLG: .WORD	AMSGLG	:: MESSAGE LENGTH
663	001214		\$ETABLE:		:: APT ENVIRONMENT TABLE
664	001214	000	\$ENV: .BYTE	AMENV	:: ENVIRONMENT BYTE
665	001215	000	\$ENVM: .BYTE	AMENVM	:: ENVIRONMENT MODE BITS
666	001216	000300	\$SWREG: .WORD	ASWREG	:: APT SWITCH REGISTER
667	001220	000000	\$USWR: .WORD	AUSWR	:: USER SWITCHES
668	001222	000000	\$CPUOP: .WORD	ACPUOP	:: CPU TYPE, OPTIONS
669			::		BITS 15-11=CPU TYPE
670			::		11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
671			::		11/70=06, PDQ=07, Q=10
672			::		BIT 10=REAL TIME CLOCK
673			::		BIT 9=FLOATING POINT PROCESSOR
674			::		BIT 8=MEMORY MANAGEMENT
675	001224	000	\$MAMS1: .BYTE	AMAMS1	:: HIGH ADDRESS, M.S. BYTE
676	001225	000	\$MTYP1: .BYTE	AMTYP1	:: MEM. TYPE, BLK#1
677			::		MEM. TYPE BYTE -- (HIGH BYTE)
678			::		900 NSEC CORE=001
679			::		300 NSEC BIPOLAR=002
680			::		500 NSEC MOS=003
681	001226	000000	\$MADR1: .WORD	AMADR1	:: HIGH ADDRESS, BLK#1
682			::		MEM. LAST ADDR. =3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
683	001230	000	\$MAMS2: .BYTE	AMAMS2	:: HIGH ADDRESS, M.S. BYTE
684	001231	000	\$MTYP2: .BYTE	AMTYP2	:: MEM. TYPE, BLK#2
685	001232	000000	\$MADR2: .WORD	AMADR2	:: MEM. LAST ADDRESS, BLK#2
686	001234	000	\$MAMS3: .BYTE	AMAMS3	:: HIGH ADDRESS, M.S. BYTE
687	001235	000	\$MTYP3: .BYTE	AMTYP3	:: MEM. TYPE, BLK#3
688	001236	000000	\$MADR3: .WORD	AMADR3	:: MEM. LAST ADDRESS, BLK#3
689	001240	000	\$MAMS4: .BYTE	AMAMS4	:: HIGH ADDRESS, M.S. BYTE
690	001241	000	\$MTYP4: .BYTE	AMTYP4	:: MEM. TYPE, BLK#4
691	001242	000000	\$MADR4: .WORD	AMADR4	:: MEM. LAST ADDRESS, BLK#4
692	001244	100400	\$VECT1: .WORD	AVECT1	:: INTERRUPT VECTOR#1, BUS PRIORITY#1
693	001246	000000	\$VECT2: .WORD	AVECT2	:: INTERRUPT VECTOR#2, BUS PRIORITY#2
694	001250	170400	\$BASE: .WORD	ABASE	:: BASE ADDRESS OF EQUIPMENT UNDER TEST
695	001252	000000	\$DEVH: .WORD	ADEVH	:: DEVICE MAP
696	001254	000000	\$CDW1: .WORD	ACDW1	:: CONTROLLER DESCRIPTION WORD#1
697	001256		\$ETEND:		
698			.NEXT		

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION SITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1: IF SITEMB IS 0 THE ONLY PERTINENT DATA IS (SERRPC).
;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ::POINTS TO THE ERROR MESSAGE
;* DH ::POINTS TO THE DATA HEADER
;* DT ::POINTS TO THE DATA
;* DF ::POINTS TO THE DATA FORMAT

SERRTB:

699									
700									
701									
702									
703									
704									
705									
706									
707									
708									
709									
710									
711									
712									
713	001256								
714									
715									
716									
717									
718	001256	014261							
719	001260	014401							
720	001262	014560							
721	001264	014620							
722									
723									
724									
725	001266	014303							
726	001270	014520							
727	001272	014610							
728	001274	014620							
729									
730									
731	001276	014327							
732	001300	014520							
733	001302	014610							
734	001304	014620							
735									
736									
737	001306	014354							
738	001310	014435							
739	001312	014572							
740	001314	014620							

;ITEM 1
EM1 ::STATUS REG. ERROR
DH1 ::ERRPC STREG EXPECTED ACTUAL
DT1 ::SERRPC, STREG, \$GDOAT, \$BDOAT
DF1

;ITEM 2
EM2 ::FAILED TO INTERRUPT
DH2 ::ERRPC STREG ACTUAL
DT2 ::SERRPC, STREG, \$BDOAT
DF1

;ITEM 3
EM3 ::UNEXPECTED INTERRUPT
DH3 ::ERRPC STREG
DT3 ::SERRPC, STREG
DF1

;ITEM 4
EM4 ::ERROR ON A/D CHANNEL
DH4 ::ERRPC STREG CHAN NOMINAL TOL ACTUAL
DT4 ::SERRPC, STREG, CHANL, \$GDOAT, SPREAD, \$BDOAT
DF1

741					.SBTTL	MISCELLANEOUS, TEMPORARY, AND STORAGE LOCATIONS
742	001316	170400			STREG:	ABASE ; ADDRESS OF STATUS REGISTER
743	001320	170401			ROST1:	ABASE+1 ; UPPER BYTE OF STATUS REG.
744	001322	170402			ROBUFT:	ABASE+2 ; ADDRESS OF A/D BUFFER
745	001324	100400			VECTOR:	AVECT1 ; VECTOR ADDRESS
746	001326	000200			BASEBR:	APRIOR ; INTERRUPT PRIORITY LEVEL
747	001330	100402			VECTR1:	AVECT1+2 ;
748	001332	100404			VECTR2:	AVECT1+4 ; ERROR VECTOR ADDRESS
749	001334	100406			VECTR3:	AVECT1+6 ;
750	001336	000004			VADR:	4 ; INCREMENT FOR BUS ADDRESS
751	001340	000010			VVCT:	10 ; INCREMENT FOR VECTOR ADDRESS
752	001342	000000			BASECH:	0 ; BASE CHANNEL
753	001344	000060			KBVECT:	60 ;
754	001346	000000			WIDE:	0 ; NO. OF WIDE STATES
755	001350	000000			NARROW:	0 ; NO. OF NARROW STATES
756	001352	000000			FIRST:	0 ;
757	001354	000000			SKIPST:	0 ; NO. OF SKIPPED STATES
758	001356	000000			TEMP:	0 ; WORK AREA
759	001360	000000			CH1:	0 ; FIRST CHANNEL
760	001362	000000			CH2:	0 ; SECOND CHANNEL
761	001364	000000			NEXT:	0 ; NO. OF ADV11'S TO BE TESTED
762	001366	000000			NTEXT:	0 ; NO. OF ADV11'S TO BE TESTED
763	001370	000000			DUMMY:	0 ; DUMMY CHANNEL
764	001372	000000			CHANL:	0 ; CHANNEL VALUE
765	001374	000000			TADDR:	0 ; TEST ADDRESS
766	001376	000000			RNA:	0 ; RANDOM
767	001400	000000			RNB:	0 ; NUMBER
768	001402	000000			RNC:	0 ; VALUES
769	001404	000000			RMS:	0 ; RMS NOISE VALUE
770	001406	000000			PEAK:	0 ; PEAK NOISE VALUE
771	001410	000000			FLAG:	0 ; VTSS FLAG
772	001412	000000			SPREAD:	0 ; DEVIATION FROM THE NOMINAL
773	001414	000000			DAC:	0 ; SAR VALUE
774	001416	000000			DELAY:	0 ; TIME DELAY COUNTER
775	001420	000000			EDGE:	0 ; EDGE VALUE
776	001422	000000			BITPNT:	0 ;
777	001424	000000			MIN:	0 ; MIN VALUE
778	001426	000000			WFTST:	0 ; OPTION TEST AREA FLAG
779	001430	000000			MAX:	0 ; MAX VALUE
780	001432	000000			PERCNT:	0 ; PERCENT FOR SAR ROUTINE
781	001434	000000			OUT:	0 ;
782	001436	000000			GUNITS:	0 ;
783	001440	000001			TSTBIT:	1 ;
784						
785	001442				UNEXP:	
786	001442	012737	001456	001162	MOV	#15, \$ESCAPE ; ; ESCAPE TO 15 ON ERROR
787	001450	005237	001103		INC	\$ERFLG
788	001454	104003			ERROR	3
789	001456	005037	001162		15:	CLR \$ESCAPE ; RETURN ESCAPE TO NORMAL
790	001462	000002			RTI	UNEXPECTED INTERRUPT

```

791          SBTTL      CONTROL A AND C DECODERS
792 001464 010046      ISERV:  MOV      RO, -(SP)      ;SAVE RO
793 001466 017700 177454  MOV      @51KB,RO      ;GET CHARACTER
794 001472 042700 177600  BIC      #177600,RO
795 001476 120027 000003  CMPB    RO,#3          ;IS IT 1C?
796 001502 001010      BNE     IS            ;ECHO CHARACTER
797 001504 104400 012036  TYPE    ,CMMSG        ;ECHO CHARACTER
798 001510 012706 001100  MOV      @STACK,SP
799 001514 004737 011300  JSR     PC,RST        ;RESET & SET INTRPT. EN.
800 001520 000137 002262  JMP     BEG2
801 001524 120027 000001  IS:    CMPB    RO,#1    ;IS IT 1A?
802 001530 001010      BNE     2S            ;ECHO CHARACTER
803 001532 104400 012031  TYPE    ,A5G          ;ECHO CHARACTER
804 001536 012706 001100  MOV      @STACK,SP
805 001542 004737 011300  JSR     PC,RST        ;RESET & SET INTRPT. EN.
806 001546 000177 177622  JMP     @TADR        ;RETURN TO TEST
807 001552 120027 000007  2S:    CMPB    RO,#7    ;IS IT 1G?
808 001556 001027      BNE     NONE
809 001560 023727 001140 177570  CMP     SWR,#177570   ;HARDWARE SWREG?
810 001566 001423      BEQ     NONE
811 001570 104400 012043  TYPE    ,CMMSG        ;ECHO CHARACTER
812 001574 017746 177340  MOV     @SWR,-(SP)    ;SAVE @SWR FOR TYPEOUT
813          ;TYPE SWREG
814 001600 104402      TYPOS   ;GO TYPE--OCTAL ASCII
815 001602 006          .BYTE   ;TYPE 6 DIGITS
816 001603 001          .BYTE   ;TYPE LEADING ZEROS
817 001604 104400 012223  TYPE    ,SLASH
818 001610 104407      RDOCT   ;READ NEW VALUE
819 001612 012677 177322  MOV     (SP)+,@SWR    ;LOAD NEW SWREG VALUE
820 001616 012600      POPRO: MOV     (SP)+,RO
821 001620 022776 000001 000000  RETURN: CMP     #1,@(SP) ;DOES IT RETURN TO A WAIT?
822 001626 001002      BNE     IS            ;NO
823 001630 062716 000002  R00     #2,(SP)      ;BUMP RETURN ADDRESS
824 001634 000002      IS:    RTI
825 001636 104400 012027  NONE:   TYPE    ,QUEST  ;TYPE "?"
826 001642 000765      BR     POPRO

```


H02

MAINDEC-11-DVADA-A
DVADAA.CMB

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INITIAL START-UP, HOUSEKEEPING, AND DIALOGUE

```

827      .SBTTL      INITIAL START-UP, HOUSEKEEPING, AND DIALOGUE
828      001644      005037      001426      BEGIN:      CLR      MFTST
829      001650      000403              BR          RBEG
830      001652      012737      000001      001426      BEGIN2:     MOV      #1, MFTST
831      001660      000005              RBEG:      RESET
832      .SBTTL      INITIALIZE THE COMMON TAGS
833      ;;CLEAR    THE COMMON TAGS (SCHTAG) AREA
834      001662      012706      001100              MOV      #SCHTAG, R6      ;;FIRST LOCATION TO BE CLEARED
835      001666      005026              CLR      (R6)+           ;;CLEAR MEMORY LOCATION
836      001670      022706      001140              CMP      #SWR, R6      ;;DONE?
837      001674      001374              BNE     -6              ;;LOOP BACK IF NO
838      001676      012706      001100              MOV      #STACK, SP     ;;SETUP THE STACK POINTER
839      ;;INITIALIZE A FEW VECTORS
840      001702      012737      015216      000020      MOV      #SCOPE, #IOTVEC  ;;IOT VECTOR FOR SCOPE ROUTINE
841      001710      012737      000340      000022      MOV      #340, #IOTVEC+2 ;;LEVEL 7
842      001716      012737      015474      000030      MOV      #ERROR, #EMTVEC  ;;EMT VECTOR FOR ERROR ROUTINE
843      001724      012737      000340      000032      MOV      #340, #EMTVEC+2 ;;LEVEL 7
844      001732      012737      017064      000034      MOV      #TRAP, #TRAPVEC  ;;TRAP VECTOR FOR TRAP CALLS
845      001740      012737      000340      000036      MOV      #340, #TRAPVEC+2;LEVEL 7
846      001746      013737      011664      011656      MOV      #ENDCT, #EOPCT  ;;SETUP END-OF-PROGRAM COUNTER
847      001754      005037      001160              CLR      $TIMES         ;;INITIALIZE NUMBER OF ITERATIONS
848      001760      005037      001162              CLR      #ESCAPE        ;;CLEAR THE ESCAPE ON ERROR ADDRESS
849      001764      012737      000001      001115      MOV      #1, #SERMAX     ;;ALLOW ONE ERROR PER TEST
850      001772      012737      001772      001106      MOV      #, #SLPADR      ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
851      002000      012737      002000      001110      MOV      #, #SLPERR      ;;SETUP THE ERROR LOOP ADDRESS
852      ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
853      ;;EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.
854      002006      013746      000004              MOV      #ERRVEC, -(SP)  ;;SAVE ERROR VECTOR
855      002012      012737      002046      000004      MOV      #64$, #ERRVEC   ;;SET UP ERROR VECTOR
856      002020      012737      177570      001140      MOV      #DSWR, SWR      ;;SETUP FOR A HARDWARE SWICH REGISTER
857      002026      012737      177570      001142      MOV      #DISP, DISPLAY  ;;AND A HARDWARE DISPLAY REGISTER
858      002034      022777      177777      177076      CMP      #-1, #SWR      ;;TRY TO REFERENCE HARDWARE SWR
859      002042      001012              BNE     66$             ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
860      ;;AND THE HARDWARE SWR IS NOT = -1
861      002044      000403              BR      65$            ;;BRANCH IF NO TIMEOUT
862      002046      012716      002054      64$:      MOV      #65$, (SP)     ;;SET UP FOR TRAP RETURN
863      002052      000002              RTI
864      002054      012737      000176      001140      65$:      MOV      #SWREG, SWR    ;;POINT TO SOFTWARE SWR
865      002062      012737      000174      001142      MOV      #DISPREG, DISPLAY
866      002070      012637      000004      66$:      MOV      (SP)+, #ERRVEC ;;RESTORE ERROR VECTOR
867
868      002074      005037      001202              CLR      $PASS         ;;CLEAR PASS COUNT
869      002100      032737      000200      001215      BITB    #APTSIZE, #ENVM  ;;TEST USER SIZE UNDER APT
870      002106      001403              BEQ     67$            ;;YES, USE NON-APT SWITCH
871      002110      012737      001216      001140      MOV      #SSWREG, SWR   ;;NO, USE APT SWITCH REGISTER
872      002116      67$:

```

873	002116	005037	001410		CLR	FLAG	;CLEAR VT55 FLAG
874	002122	005737	000042		TST	#42	;IS IT CHAINED?
875	002126	001033			BNE	REST1	
876						DETERMINE IF	VT55 TYPE TERMINAL IS PRESENT
877	002130	042777	000100	177006	.SBTTL	#100, #5TKS	
878	002136	104400	013724		BIC	CO	;TYPE ASCIZ STRING
879	002142	004737	002432		TYPE	PC, VTFLG	;GET A CHARACTER
880	002146	020027	000033		JSR	RO, #33	
881	002152	001017			CMP	NOVT55	;NO VT55 PRESENT
882	002154	004737	002432		BNE	PC, VTFLG	;GET A CHARACTER
883	002160	020027	000057		JSR	RO, #57	
884	002164	001012			CMP	NOVT55	;NO VT55 PRESENT
885	002166	004737	002432		BNE	PC, VTFLG	;GET A CHARACTER
886	002172	020027	000103		JSR	RO, #103	
887	002176	001403			CMP	VT55	;VT55 IS PRESENT
888	002200	020027	000105		BEQ	RO, #105	
889	002204	001002			CMP	NOVT55	
890	002206	005237	001410		BNE	FLAG	
					VT55:	INC	

```

891      SBTTL      DIALOGUE TO DETERMINE WHICH TEST TO RUN
892      NOV155:   TYPE      ,READ1
893      REST1:   RESET
894      JSR      PC, FIXONE      ;INITIALIZE ADDRESSES
895      MOV      KEVECT, R0
896      MOV      #ISERV, (R0)+
897      MOV      #340, (R0)
898      MOV      #62341, RMA      ;RANDOM NO, VARIABLES
899      MOV      #142315, RMB
900      MOV      #127623, RMC
901      BEG2:    MOV      #STACK, SP      ;RESET STACK POINTER INCASE RESTARTED
902      RESET
903      TST      #42
904      BEQ      IS
905      JMP      BEGL      ;GO TO LOGIC TESTS
906      TST      #TEST
907      BNE
908      TYPE
909      TRYAG:   ROLIN      ,MSG71
910      BIS      #100, #STKS
911      CLR      -(SP)      ;CLEAR PSW
912      MOV      #15, -(SP)
913      RTI
914      MOV      (SP)+, R0      ;READ ANSWER
915      BICB    #40, (R0)
916      CMPB    (R0), #'A      ;IS IT A?
917      BNE
918      JMP      BEGINA     ;;NO, TRY C
919      CMPB    (R0), #'C      ;GO TO AUTO TEST
920      BNE
921      JMP      BEGINC     ;IS IT C?
922      CMPB    (R0), #'P      ;;NO, TRY P
923      BNE
924      JMP      BEGINP     ;GO TO CALIBRATION TEST
925      CMPB    (R0), #'L      ;IS IT P?
926      BNE
927      JMP      BEGL      ;;NO, TRY L
928      CMPB    (R0), #'W      ;GO TO DISPLAY CONVERSIONS TEST
929      BNE
930      JMP      BEGL      ;IS IT L?
931      TYPE    6S
932      BR      QUEST      ;;NO, TRY W
                                ;GO TO LOGIC TESTS
                                ;IS IT W?
                                ;;NO, TRY AGAIN
                                ;GO TO WRAPAROUND TEST
                                ;WAIT FOR CHARACTER

```

933	002432	005000		VTFLG:	CLR	RO		; TEST FOR PRESENCE
934	002434	105777	176504	1\$:	TSTB	2\$TKS		; OF VT55
935	002440	100404			BMI	2\$; ;VT55 RESPONDS WITH <33><57>[<103> OR <105>]
936	002442	005300			DEC	RO		
937	002444	001373			BNE	1\$		
938	002446	005726			TST	(SP)+		; POP A WORD OFF STACK
939	002450	000660			BR	NOVT55		; ;NO VT55 PRESENT
940	002452	017700	176470	2\$:	MOV	2\$TKB,RO		
941	002456	042700	177600		BIC	177600,RO		; TEST VT55 CODE
942	002462	000207			RTS	PC		
943								
944	002464	005037	001202	TESTAD:	CLR	\$PASS		; CLEAR PASS COUNT
945	002470	005037	001436		CLR	GUNITS		; CLEAR UNIT ERROR BITS
946	002474	012737	000001	001440	MOV	#1,TSTBIT		; INITIALIZE MODULE ERROR TEST BIT
947	002502	012737	000001	001356	MOV	#1,TEMP		; SET UP FOR ONLY ONE A/D
948	002510	105737	001215		TSTB	\$ENVM		; TESTING ONLY ONE A/D?
949	002514	100411			BMI	3\$; YES
950	002516	012737	000004	001356	MOV	#4,TEMP		; SET UP MAX NO OF A/D'S
951	002524	005737	001426		TST	HFTEST		; IS IT IN OPTION TEST
952	002530	001403			BEG	3\$; NOT IN OPTION TEST
953	002532	012737	000020	001356	MOV	#16,TEMP		; SET UP OPTION MAX NO OF A/D'S
954	002540	013737	001250	001126	3\$:	MOV	\$BASE,\$BDDAT	; SETUP TO TEST FOR ADV11'S
955	002546	013746	000004		MOV	2\$ERRVEC,-(SP)		; SAVE ERRVEC
956	002552	012737	002624	000004	MOV	2\$ERRVEC		; SET UP FOR TIME OUT ERROR
957	002560	005037	001364		CLR	NBEXT		; CLEAR ADV11 COUNTER
958	002564	005777	176336	1\$:	TST	2\$BDDAT		; ADDRESS ADV11
959	002570	005237	001364		INC	NBEXT		; INCREMENT ADV11 COUNTER
960	002574	053737	001440	001436	BIS	TSTBIT,GUNITS		; SET A/D BIT UNDER TEST
961	002602	006337	001440		ASL	TSTBIT		; SET TEST BIT FOR NEXT UNIT
962	002606	005337	001356		DEC	TEMP		; REACHED MAX?
963	002612	001405			BEG	4\$; REACHED MAX NO OF A/D'S
964	002614	063737	001336	001126	ADD	VADR,\$BDDAT		; GET NEXT ADV11
965	002622	000760			BR	1\$; TRY NEXT ADV11
966	002624	022626		2\$:	CMP	(SP)+,(SP)+		; POP 2 WORDS OFF STACK
967	002626			4\$:				
968	002626	013746	001364		MOV	NBEXT,-(SP)		; SAVE NBEXT FOR TYPEOUT
969								; TYPE NUMBER OF ADV11'S
970	002632	104402		TYPOS				; GO TYPE--OCTAL ASCII
971	002634	002		.BYTE	2			; TYPE 2 DIGIT(S)
972	002635	000		.BYTE	0			; SUPPRESS LEADING ZEROS
973	002636	104410	013101	TYPE	MSG50			
974	002642	005317	001364	DEC	NBEXT			; ADJUST ADV11 COUNT
975	002646	013737	001364	001366	MOV	NBEXT,NBEXT		; KEEP COUNT OF NUMBER
976	002654	012637	000004		MOV	(SP)+,ERRVEC		; RESTORE ERRVEC
977	002660	012737	000001	001440	MOV	#1,TSTBIT		; INITIALIZE MODULE ERROR TEST BIT
978	002666	000207			RTS	PC		

```

979 002670 BEGINL:
980 ;*****
981 ;*TEST 1 FLOAT A ONE THRU MULTIPLEXER BITS
982 ;*****
983 002670 012737 002670 001106 †ST1: MOV #TST1,$LPAOR
984 002676 012737 002670 001110 MOV #TST1,$LPERR
985 002704 012737 000400 001124 MOV #BIT8,$GDDAT ;LOAD FIRST BIT
986 002712 104411 2S: CHKIT ;
987 002714 104001 ERROR 1 ;FAILED TO LOAD + READ BIT
988 002716 006337 001124 1S: ASL $GDDAT ;GET NEXT BIT
989 002722 023737 001124 010000 CMP $GDDAT,#BIT12 ;FINISHED?
990 002730 001370 BNE 2S ;;NO,GO TO NEXT TEST
991
992 ;*****
993 ;*TEST 2 LOAD AND READ BACK ERROR I.E. BIT14
994 ;*****
995 002732 000004 †ST2: SCOPE
996 002734 012737 040000 001124 MOV #BIT14,$GDDAT
997 002742 104411 CHKIT
998 002744 104001 ERROR 1 ;FAILED TO LOAD + READ ERROR I.E.
999
1000 ;*****
1001 ;*TEST 3 LOAD AND READ BACK INTERRUPT ENABLE BIT6
1002 ;*****
1003 002746 000004 †ST3: SCOPE
1004 002750 012777 001442 176346 MOV #UNEXP,$VECTOR ;SETUP FOR UNEXPECTED INTERUPT
1005 002756 012737 000100 001124 MOV #BIT6,$GDDAT ;LOAD EXPECTED DATA
1006 002764 104411 CHKIT
1007 002766 104001 ERROR 1 ;FAILED TO LOAD + READ INTERRUPT ENABLE
1008
1009 ;*****
1010 ;*TEST 4 LOAD AND READ BACK CLOCK OVERFLOW START ENABLE BITS
1011 ;*****
1012 002770 000004 †ST4: SCOPE
1013 002772 012737 000040 001124 MOV #BIT5,$GDDAT ;LOAD EXPECTED DATA
1014 003000 104411 CHKIT
1015 003002 104001 ERROR 1 ;FAILED TO LOAD + READ CLOCK OVERFLOW START ENAB
1016
1017 ;*****
1018 ;*TEST 5 LOAD AND READ BACK EXTERNAL START ENABLE BIT4
1019 ;*****
1020 003004 000004 †ST5: SCOPE
1021 003006 012737 003020 001124 MOV #BIT4,$GDDAT ;LOAD EXPECTED DATA
1022 003014 104411 CHKIT
1023 003016 104001 ERROR 1 ;FAILED TO LOAD + READ EXT. START ENABLE
1024
1025 ;*****
1026 ;*TEST 6 LOAD AND READ BACK MAINT. TST BIT2
1027 ;*****
1028 003020 000004 †ST6: SCOPE
1029 003022 012737 000004 001124 MOV #BIT2,$GDDAT
1030 003030 104411 CHKIT
1031 003032 104001 ERROR 1 ;FAILED TO LOAD + READ BACK MAINT. TST

```

M02

MAINDEC-11-DVADA-A
DVADAA.CMB T7

MACY11 27(732) 21-OCT-76 11:18 PAGE 26
LOAD AND READ BACK ENABLE I.D. BIT3

```

1030 ;*****
1031 ;*TEST 7      LOAD AND READ BACK ENABLE I.D. BIT3
1032 ;*****
1033 003034 000004          †ST7:  SCOPE
1034 003036 012737 000010 001124      MOV      #BIT3,$GDDAT
1035 003044 104411          CHKIT
1036 003046 104001          ERROR      1          ;FAILED TO LOAD + READ ENABLE I.D. BIT
1037 ;*****
1038 ;*TEST 10     TEST I.D. BIT (BIT 12) CLEARED
1039 ;*****
1040 003050 000004          †ST10: SCOPE
1041 003052 012777 000001 176236      MOV      #1,‡STREG      ;CLEAR I.D. ENABLE
1042 003060 105777 176232 1$:      TSTB     ‡STREG      ;WAIT FOR CONVERSION
1043 003064 100375          BPL      1$           ;CONVERSION IS NOT DONE YET
1044 003066 032777 010000 176226      BIT      #BIT12,‡ADDBUFF ;IS I.D. BIT CLEARED?
1045 003074 001401          BEQ      TST11        ;;YES - GOTO NEXT TEST
1046 003076 104001          ERROR      1
1047 ;*****
1048 ;*TEST 11     TEST I.D. BIT (BIT 12) SET
1049 ;*****
1050 003100 000004          †ST11: SCOPE
1051 003102 012777 000011 176206      MOV      #BIT13!BIT0,‡STREG ;SET I.D. ENABLE BIT
1052 003110 105777 176202 1$:      TSTB     ‡STREG      ;WAIT FOR CONVERSION
1053 003114 100375          BPL      1$           ;CONVERSION IS NOT DONE YET
1054 003116 032777 010000 176176      BIT      #BIT12,‡ADDBUFF ;IS I.D. BIT SET?
1055 003124 001001          BNE     TST12        ;;YES - GOTO NEXT TEST
1056 003126 104001          ERROR      1
1057 ;*****
1058 ;*TEST 12     LOAD AND READ BACK ERROR FLAG BIT15
1059 ;*****
1060 003130 000004          †ST12: SCOPE
1061 003132 012737 100000 001124      MOV      #BIT15,$GDDAT    ;LOAD EXPECTED DATA
1062 003140 104411          CHKIT
1063 003142 104001          ERROR      1          ;FAILED TO LOAD + READ ERROR FLAG
1064 ;*****
1065 ;*TEST 13     TEST INIT CLEARS BITS 2-6,8-11,14
1066 ;*****
1067 003144 000004          †ST13: SCOPE
1068 003146 012737 000300 001160      MOV      #300,$TIMES     ;DO 300 ITERATIONS
1069 003154 005037 001124          CLR      $GDDAT         ;LOAD EXPECTED DATA
1070 003160 012777 047574 176130 2$:  MOV      #47574,‡STREG   ;SET STATUS REGISTER
1071 003166 000005          RESET          ;INITIALIZE
1072 003170 052777 000100 175746      BIS      #100,‡STKS     ;SET INTRPT. ENABLE
1073 003176 017737 176114 001126      MOV      ‡STREG,$BDDAT  ;READ STATUS REGISTER
1074 003204 001401          BEQ      TST14        ;NEXT TEST
1075 003206 104001          ERROR      1          ;RESET FAILED TO CLEAR AD ST. REG. BITS
1076
1077
1078

```



```

1079
1080
1081
1082 003210 000004
1083 003212 0.2737 000300 001160
1084 003220 012777 100000 176070
1085 003226 000005
1086 003230 052777 000100 175706
1087 003236 104410
1088 003240 104001
1089
1090
1091
1092 003242 000004
1093 003244 012700 001000
1094 003250 005277 176042
1095 003254 012737 000200 001124
1096 003262 005300
1097 003264 001376
1098 003266 042777 100000 176022
1099 003274 104410
1100 003276 104001
1101 003300 017700 176016

;*****
;TEST 14 TEST INIT CLEARS ERROR FLAG
;*****
↑ST14: SCOPE
MOV #300, %TIMES ;DO 300 ITERATIONS
MOV #BIT15, %STREG ;SET BIT 15
RESET ;ISSUE INIT
BIS #100, %STKS ;SET INTRPT. EN. FOR KEYBOARD
CHECK
ERROR 1

;*****
;TEST 15 TEST DONE FLAG SETS AND BIT0 CLEARS ON END OF CONV.
;*****
↑ST15: SCOPE
MOV #BIT9, R0 ;STALL TIME COUNTER
INC %STREG ;START CONVERSION
MOV #BIT7, %GDDAT ;LOAD EXPECTED
1$: DEC R0 ;STALL
BNE 1$ ;TIME
BIC #BIT15, %STREG ;MASK OUT ERROR BIT
CHECK
ERROR 1 ;A/D DONE FLAG FAILED TO SET; BIT0 FAILED TO CLEAR
MOV %POBUFF, R0 ;CLEAR DONE FLAG FOR ITERATIONS

```

```

1102      ;*****
1103      ;*TEST 16      TEST INIT CLEARS DONE FLAG
1104      ;*****
1105      003304 000004      †ST16: SCOPE
1106      003306 012737 000300 001160      MOV      #300,STIMES      ;DO 300 ITERATIONS
1107      003314 005037 001124      CLR      $G00AT      ;CLEAR EXPECTED
1108      003320 005277 175772      INC      @STREG      ;START CONVERSION
1109      003324 105777 175766      2$:     TSTB     @STREG
1110      003330 100375      BPL     2$
1111      003332 000005      RESET
1112      003334 104410      CHECK
1113      003336 104001      ERROR 1      ;DONE FLAG FAILED '0 CLEAR
1114      003340 052777 000100 175576      BIS     #100,@STKS      ;SET INTRPT. EN. BIT
1115
1116      ;*****
1117      ;*TEST 17      TEST A/D DONE FLAG CLEARS WHEN READ CONVERTED VALUE
1118      ;*****
1119      003346 000004      †ST17: SCOPE
1120      003350 005277 175742      INC      @STREG      ;SET A/D START CONVERSION BIT
1121      003354 105777 175736      1$:     TSTB     @STREG      ;WAIT FOR FLAG
1122      003360 100375      BPL     1$
1123      003362 017700 175734      MOV     @A08BUF,R0      ;READ CONVERTED VALUE
1124      003366 104410      CHECK
1125      003370 104001      ERROR 1      ;DONE FLAG FAILED TO CLEAR
1126
1127      ;*****
1128      ;*TEST 20      TEST ALL '0'S RESULTS USING MAINT. ADTST. BIT
1129      ;*****
1129      003372 000004      †ST20: SCOPE
1130      003374 005037 001124      CLR     $G00AT      ;CLEAR EXPECTED VALUE
1131      003400 005037 001372      CLR     CHANL      ;SET CHANL = 0
1132      003404 005037 001412      CLR     SPREAD     ;SET SPREAD = 0
1133      003410 012777 000005 175700      MOV     #5,@STREG   ;CONVERT EVEN CHANNEL WITH MAINT. BIT SET
1134      003416 105777 175674      1$:     TSTB     @STREG      ;WAIT FOR DONE
1135      003422 100375      BPL     1$
1136      003424 017737 175672 001126      MOV     @A08BUF,$B00AT ;RESULTS TO B00AT FOR CHECKING
1137      003432 001401      BEQ     TST21      ;GOTO NEXT TEST
1138      003434 104004      ERROR 4      ;DID NOT GET ALL '0'S RESULT WITH MAINT. ADTST
1139
1140      ;*****
1141      ;*TEST 21      TEST ALL '1'S RESULT USING MAINT. ADTST. BIT
1142      ;*****
1143      003436 000004      †ST21: SCOPE
1144      003440 012737 007777 001124      MOV     #7777,$G00AT ;EXPECT ALL '1'S RESULT
1145      003446 012737 000001 001372      MOV     #1,CHANL     ;SET CHANL = 1
1146      003454 005037 001412      CLR     SPREAD     ;SET SPREAD = 0
1147      003460 012777 000405 175630      MOV     #405,@STREG ;CONVERT ODD CHANNEL WITH MAINT. BIT SET
1148      003466 105777 175624      1$:     TSTB     @STREG      ;WAIT FOR DONE
1149      003472 100375      BPL     1$
1150      003474 017737 175622 001126      MOV     @A08BUF,$B00AT ;RESULTS TO B00AT FOR CHECKING
1151      003502 023737 001124 001126      CMP     $G00AT,$B00AT ;EQUAL?
1152      003510 001401      BEQ     TST22      ;GOTO NEXT TEST
1153      003512 104004      ERROR 4      ;DID NOT GET ALL '1'S RESULT WITH MAINT. ADTST

```

C03

MAINDEC-11-DVADA-A
DVADA.CMB T22

MACY 27(732) 21-OCT-76 11:18 PAGE 29
GENERATE INTERRUPT WHEN DONE FLAG SETS AFTER CONVERSION

```

1154 ;*****
1155 ;*TEST 22 GENERATE INTERRUPT WHEN DONE FLAG SETS AFTER CONVERSION
1156 ;*****
1157 003514 000004 †ST22: SCOPE
1158 ;* "ENTERING TEST 22" TYPED OUT TO TELL YOU THE NEXT
1159 ;*TEST THAT IS GOING TO BE EXECUTED. IT IS ONLY TYPED ON PASS 0.
1160 ;*THERE IS DANGER THAT THE UNIBUS COULD GET "HUNG" WHILE
1161 ;*EXECUTING TEST "22".
1162 003516 012700 000022 MOV #22,R0 ;GET TEST NO.
1163 003522 004737 011176 JSR PC,DUMW ;PRINT MESSAGE
1164 003526 005046 CLR -(SP) ;RESET PRIORITY
1165 003530 012746 003536 MOV #3$,-(SP)
1166 003534 000002 RTI
1167 003536 012777 003612 175560 3$: MOV #1$,@VECTOR ;INTERRUPT VECTOR ADDRESS
1168 003544 012777 000200 175556 MOV #200,@VECTOR1 ;SET UP NEW PSW
1169 003552 012777 000101 175536 MOV #BIT6!BIT0,@STREG ;SET INTERRUPT ENABLE BIT + START CONVERSION
1170 003560 105777 175532 2$: TSTB @STREG ;WAIT FOR DONE
1171 003564 100375 BPL 2$ ;FLAG TO SET
1172 003566 017737 175524 001126 MOV @STREG,$BOOAT ;READ STATUS REGISTER
1173 003574 012737 000300 001124 MOV #BIT7!BIT6,$GDOAT ;GOOD DATA
1174 003602 104002 ERROR 2 ;FAILED TO INTERRUPT ON DONE
1175 003604 004737 011250 JSR PC,DUMC ;TYPE COMPLETED
1176 003610 000414 BR TST23 ;BRANCH TO NEXT TEST
1177 003612 022626 1$: CMP (SP)+,(SP)+ ;RESET STACK POINTER
1178 003614 012777 001442 175502 MOV #UNEXP,@VECTOR ;SET UP FOR UNEXPECTED INTERRUPT
1179 003622 005046 CLR -(SP) ;CLEAR PSW
1180 003624 012746 003632 MOV #4$,-(SP)
1181 003630 000002 RTI
1182 003632 004737 011250 4$: JSR PC,DUMC ;TYPE COMPLETED
1183 003636 005777 175460 TST @A0BUFF ;CLEAR DONE BIT
1184 ;*****
1185 ;*TEST 23 TEST INTERRUPT OCCURS WHEN ERROR AND I.I.E. IS SET
1186 ;*****
1187 003642 000004 †ST23: SCOPE
1188 ;* "ENTERING TEST 23" TYPED OUT TO TELL YOU THE NEXT
1189 ;*TEST THAT IS GOING TO BE EXECUTED. IT IS ONLY TYPED ON PASS 0.
1190 ;*THERE IS DANGER THAT THE UNIBUS COULD GET "HUNG" WHILE
1191 ;*EXECUTING TEST "23".
1192 003644 012700 000023 MOV #23,R0 ;GET TEST NO.
1193 003650 004737 011176 JSR PC,DUMW ;PRINT MESSAGE
1194 003654 012777 003714 175450 MOV #1$,@VECTOR2 ;SETUP VECTOR ADDRESS
1195 003662 012777 140000 175426 MOV #BIT15!BIT14,@STREG ;CAUSE AN INTERRUPT
1196 003670 017737 175422 001126 MOV @STREG,$BOOAT ;BAD DATA
1197 003676 012737 140000 001124 MOV #BIT15!BIT14,$GDOAT ;GOOD DATA
1198 003704 104002 ERROR 2
1199 003706 004737 011250 JSR PC,DUMC ;TYPE COMPLETED
1200 003712 003627 BR TST20
1201 003714 022626 1$: CMP (SP)+,(SP)+ ;POP STACK
1202 003716 004737 011250 JSR PC,DUMC
1203 003722 005077 175370 CLR @STREG

```

D03

MAINDEC-11-DVADA-A
DVADAA.CMB T24

MACY11 27(732) 21-OCT-76 11:18 PAGE 30
TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER

```

1204
1205
1206
1207 003726 000004
1208 003730 012777 000001 175360
1209 003736 105777 175354
1210 003742 100375
1211 003744 012737 100200 001124
1212 003752 012777 000001 175356
1213 003760 012700 001000
1214 003764 005300
1215 003766 001376
1216 003770 104410
1217 003772 104001
1218
1219 003774 017700 175322
1220
1221
1222
1223 004000 000004
1224 004002 012737 100000 001124
1225 004010 012777 000001 175300
1226 004016 112777 000001 175272
1227 004024 112777 000001 175264
1228 004032 017737 175260 001126
1229 004040 042737 077777 001126
1230 004046 023737 001124 001126
1231 004054 001401
1232 004056 104001
1233
1234 004060 017700 175236
1235 004064 005077 175226
1236 004070 000004
1237 004072 000207
1238
1239
1240
1241 004074 013777 001124 175214
1242 004102 017737 175210 001126
1243 004110 023737 001124 001126
1244 004116 001002
1245 004120 062716 000002
1246 004124 000002

::*****
;TEST 24 TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER
::*****
TST24: SCOPE
MOV @BIT0,@STREG ;START CONVERSION
15: TSTB @STREG ;WAIT FOR
BPL 15
25: MOV @BIT15:BIT7,$GDDAT ;LOAD EXPECTED VALUE
MOV @BIT0,@STREG ;START 2ND CONVERSION
MOV @BIT9,R0 ;WAIT FOR 2ND
35: DEC R0 ;CONVERSION TO END
BNE 35
45: CHECK ERROR 1 ;ERROR FLAG NOT SET WHEN 2ND
; CONVERT ENDS BEFORE READ BUFFER FROM FIRST
; CLEAR DONE FLAG
MOV @D0BUFF,R0
::*****
;TEST 25 TEST ERROR FLAG SETS IF START 2ND CONV. BEFORE DONE FLAG SETS
::*****
TST25: SCOPE
MOV @BIT15,$GDDAT ;LOAD EXPECTED DATA
MOV @BIT0,@STREG ;START CONVERSION
MOV @BIT0,@STREG ;START NEXT CONVERSION
MOV @BIT0,@STREG ;ONCE AGAIN IN CASE RE-FRESH INTERVENED
MOV @STREG,$B0DAT ;READ STATUS REGISTER
BIC @77777,$B0DAT ;MASK OUT BIT 15
CMP $GDDAT,$B0DAT ;COMPARE RESULTS
BEQ 15 ;BRANCH OVER ERROR
ERROR 1 ;ERROR FLAG NOT SET WHEN 2ND
; CONVERT BEGINS BEFORE FIRST DONE
; READ CONVERTED VALUE
; CLEAR STATUS REGISTER
15: MOV @D0BUFF,R0
CLR @STREG
SCOPE
RTS PC ;RETURN TO TEST SECTION

::SUBROUTINE FOR LOGIC TESTS:;
TESTIT: MOV $GDDAT,@STREG ;LOAD EXPECTED VALUE:
TEST: MOV @STREG,$B0DAT ;READ ST. REG.
CMP $GDDAT,$B0DAT ;COMPARE RESULTS
BNE RETERR ;;ERROR RETURN
ADD @2,(SP) ;BUMP RETURN ADDRESS TO GET AROUND ERROR
RETERR: RTI

```

E03

MAINDEC-11-DVADA-A
DVADA.CMB

MACY11 27(732) 21-OCT-76 11:18 PAGE 31
WRAPAROUND TEST SECTION

WRAPAROUND TEST SECTION

1247				
1248	004126			
1249				
1250				
1251				
1252	004126	012737	000026	001102
1253	004134	012737	000010	001160
1254	004142	012737	004126	001110
1255	004150	012737	004126	001106
1256	004156	004537	011016	
1257	004162	000000		
1258	004164	004537	011130	
1259	004170	004000		
1260	004172	011576		
1261	004174	104004		
1262				
1263				
1264				
1265	004176	000004		
1266	004200	012737	000010	001160
1267	004206	004537	011016	
1268	004212	000001		
1269	004214	004537	011130	
1270	004220	007344		
1271	004222	011602		
1272	004224	104004		
1273				
1274				
1275				
1276				
1277	004226	000004		
1278	004230	012737	000010	001160
1279	004236	004537	011016	
1280	004242	000002		
1281	004244	004537	011130	
1282	004250	000434		
1283	004252	011602		
1284	004254	104004		
1285				
1286				
1287				
1288	004256	000004		
1289	004260	012737	000010	001160
1290	004266	012737	000004	004300
1291	004274	004537	011016	
1292	004300	000004		
1293	004302	004537	011130	
1294	004306	004000		
1295	004310	011576		
1296	004312	104004		
1297	004314	005237	004300	
1298	004320	022737	000017	004300
1299	004326	001362		

```

.SBTL
WRAP:
*****
*TEST 26      TEST CH0 GROUND
*****
TST26:  MOV      #STN,STSTM
        MOV      #10,STIMES      ;;DO 10 ITERATIONS
        MOV      #TST26,SLPERR
        MOV      #TST26,SLPADR
        JSR      RS,CONVRT      ;CONVERT 8 TIMES
        0
        JSR      RS,COMPAR      ;COMPARE RESULTS
        4000      ;NOMINAL
        V12      ;TOLERANCE
        ERROR    4      ;ERROR ON A/D CHANNEL
*****
*TEST 27      TEST CH1 +4.5 VOLT
*****
TST27:  SCOPE
        MOV      #10,STIMES      ;;DO 10 ITERATIONS
        JSR      RS,CONVRT      ;CONVERT 8 TIMES
        1
        JSR      RS,COMPAR      ;COMPARE RESULTS
        7344      ;NOMINAL
        V326     ;TOLERANCE
        ERROR    4      ;ERROR ON A/D CHANNEL
*****
*TEST 30      TEST CH2 -4.5 VOLT
*****
TST30:  SCOPE
        MOV      #10,STIMES      ;;DO 10 ITERATIONS
        JSR      RS,CONVRT      ;CONVERT 8 TIMES
        2
        JSR      RS,COMPAR      ;COMPARE RESULTS
        434      ;NOMINAL
        V326     ;TOLERANCE
        ERROR    4      ;ERROR ON A/D CHANNEL
*****
*TEST 31      TEST GROUND ON CHANNELS 4 - 17
*****
TST31:  SCOPE
        MOV      #10,STIMES      ;;DO 10 ITERATIONS
        MOV      #4,CS          ;SET UP FIRST CHANNEL
        JSR      RS,CONVRT      ;CONVERT CHANNEL
        4
        JSR      RS,COMPAR      ;TEST RESULTS
        4000
        V12
        ERROR    4
        INC      CS          ;GET NEXT CHANNEL
        CMP      #17,CS        ;DONE?
        BNE     CS          ;;NO

```

```

1300
1301
1302
1303 004330 000004
1304 004332 012737 000001 001160
1305 004340 005077 174756
1306 004344 004537 011016
1307 004350 000000
1308 004352 013704 001356
1309 004356 012777 000377 174736 15:
1310 004364 004537 011016
1311 004370 000000
1312 004372 160437 001356
1313 004376 004537 011130
1314 004402 000005
1315 004404 011572
1316 004406 104004
1317
1318
1319
1320 004410 000004
1321 004412 012737 000001 001160
1322 004420 013737 001342 001372
1323 004426 013737 001342 001370
1324 004434 004737 005150
1325 004440 104400 013736
1326 004444 004737 005234
1327 004450 004537 011130
1328 004454 000000
1329 004456 011600
1330 004460 000401
1331 004462 000403
1332 004464 104400 012547 OFFERR: TYPE
1333 004470 000402
1334 004472 104400 012225 OFFOK: TYPE

*****
*TEST 32 TEST VERNIER OFFSET DAC ON CHO
*****
↑ST32: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
CLR 2A0BUFF ;SET VERNIER DAC = 0
JSR RS,CONVRT ;CONV. CHO, DIRECT VERNIER DAC
0
MOV TEMP,R4 ;SAVE VALUE IN R4
MOV #377,2A0BUFF ;SET VERNIER DAC = 377
JSR RS,CONVRT ;CONVERT IT
0
SUB R4,TEMP ;TEMP=DIFF. BETWEEN VALUE & PREVIOUS
JSR RS,COMPAR ;COMPARE RESULTS
5
V2
ERROR 4

*****
*TEST 33 OFFSET ON CHO
*****
↑ST33: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
MOV BASECH,CHANL ;LOAD CHANNEL
MOV BASECH,DUMMY ;LOAD DUMMY
JSR PC,OFFSET ;FIND OFFSET
TYPE 0,OFFSET ;TYPE "OFFSET="
JSR PC,TOFF ;TYPE OFFSET
JSR RS,COMPAR ;IS RESULT WITHIN LIMITS?
0
VS00
BR OFFERR ;NO-ERROR
BR OFFOK ;YES-OK
TYPE ERMSG
↑ST34
BR ,OKMSG ;;GO TO NEXT TEST

```



```

1335                                     ;*****
1336                                     ;*TEST 34      TEST RAMP RANGE, CH3
1337                                     ;*****
1338 004476 000004 1ST34: SCOPE
1339 004500 012737 000001 001160  MOV      #1,STIMES      ;DO THIS ONCE
1340 004506 012703 007777          MOV      #7777,R3      ;INIT R3 VALUE
1341 004512 005064          CLR      R4           ;AND R4
1342 004514 012777 001400 174574  MOV      #1400,@STREG  ;SETUP FOR CH3
1343 004522 012702 047040          MOV      #20000.,R2   ;SETUP FOR 20,000 CONVERSIONS
1344 004526 105277 174564          1$: INCB   @STREG
1345 004532 105777 174560          2$: TSTB   @STREG
1346 004536 100375          BPL     2$
1347 004540 027704 174556          CMP     @A0BUFF,R4
1348 004544 003402          BLE     3$
1349 004546 017704 174550          MOV     @A0BUFF,R4   ;HIT A NEW HIGH
1350 004552 027703 174544          3$: CMP     @A0BUFF,R3
1351 004556 002002          BGE     4$
1352 004560 017703 174536          MOV     @A0BUFF,R3   ;HIT A NEW LOW
1353 004564 005302          4$: DEC     R2
1354 004566 001357          BNE     1$
1355 004570 010337 001356          MOV     R3,TEMP
1356 004574 004537 011130          JSR     R5,COMPAR
1357 004600 000000          0
1358 004602 011570          VD
1359 004604 104004          ERROR   4           ;RAMP DIDN'T REACH LOW END OF RANGE
1360 004606 010437 001356          MOV     R4,TEMP
1361 004612 004537 011130          JSR     R5,COMPAR
1362 004616 007777          7777
1363 004620 011570          VD
1364 004622 104004          ERROR   4           ;RAMP DIDN'T REACH HIGH END OF RANGE

```

```

1365                                     ::*****
1366                                     ::*TEST 35      NOISE TEST, 1 EDGE
1367                                     ::*****
1368 004624 000004 1ST35: SCOPE
1369 004626 012737 000001 001160 MOV      #1, $TIMES      ;;DO 1 ITERATION
1370 004634 104400 011764 TYPE      NOIMSG
1371 004640 005037 001372 CLR      CHANL          ;LOAD CHANNEL 0
1372 004644 013737 001372 001370 15: MOV      CHANL, DUMMY    ;LOAD DUMMY CHANNEL
1373 004652 004737 006714 JSR      PC, GETEDG    ;GET EDGE VALUE
1374 004656 005037 001404 CLR      RMS           ;CLEAR RMS VALUE
1375 004662 005037 001406 CLR      PEAK          ;CLEAR PEAK VALUE
1376 004666 004537 007074 JSR      RS, SRSUB     ;DO SAR ROUTINE AT 16%
1377 004672 000020 16.
1378 004674 063737 001414 001404 ADD      DAC, RMS      ;ADD RESULT TO RMS
1379 004702 004537 007074 JSR      RS, SRSUB     ;DO SAR ROUTINE AT 84%
1380 004706 000124 84.
1381 004710 163737 001414 001404 SUB      DAC, RMS      ;SUBTRACT RESULT FROM RMS
1382 004716 004537 007074 JSR      RS, SRSUB     ;DO SAR ROUTINE AT 1%
1383 004722 000001 1.
1384 004724 063737 001414 001406 ADD      DAC, PEAK     ;ADD RESULT TO PEAK
1385 004732 004537 007074 JSR      RS, SRSUB     ;DO SAR ROUTINE AT 99%
1386 004736 000143 99.
1387 004740 163737 001414 001406 SUB      DAC, PEAK     ;SUBTRACT RESULT FROM PEAK
1388 004746 012737 000001 007072 MOV      #1, EDGFLG
1389 004754 004737 010666 JSR      PC, TYPRP    ;TYPE RMS AND PEAK VALUES
1390 004760 005237 001372 INC      CHANL        ;GET NEXT CHANNEL
1391 004764 022737 000003 001372 CMP      #3, CHANL    ;CHANNEL 3?
1392 004772 001002 BNE     2$           ;;NO
1393 004774 005237 001372 INC      CHANL        ;CHANNEL 3 IS SKIPE
1394 005000 022737 000017 001372 25: CMP      #17, CHANL   ;DONE?
1395 005006 001316 BNE     1$           ;;NO

```

```

1396
1397
1398
1399 005010 000004
1400 005012 012737 007001 001160
1401 005020 104400 012003
1402 005024 012737 000001 001360
1403 005032 012737 000002 001362
1404 005040 013737 001362 001372 1S:
1405 005046 004737 006714
1406 005052 005002
1407 005054 004737 006652
1408 005060 004737 006652
1409 005064 100001
1410 005066 005402
1411 005070 010204 2S:
1412 005072 012737 000001 007072
1413 005100 004737 006522
1414 005104 022737 000002 001360
1415 005112 001410
1416 005114 013702 001360
1417 005120 013737 001362 001360
1418 005126 010237 001362
1419 005132 000742
1420 005134
1421
1422
1423
1424 005134 000004
1425 005136 012737 000001 001160
1426 005144 005737 001202
1427 005150 001402
1428 005152 004737 007266
1429 005156 000207
1430
1431 005160 012737 004001 001420 OFFSET:
1432 005166 004537 007074
1433 005172 000062
1434 005174 013737 001414 001356
1435 005202 012737 004000 001420
1436 005210 004537 007074
1437 005214 000062
1438 005216 063737 001414 001356
1439 005224 162737 000400 001356
1440 005232 000207

```

```

*****
*TEST 36 INTERCHANNEL SETTLING TEST, 1 EDGE
*****
TST36: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
TYPE SETMSG ;TYPE "SETTLING TEST"
;DO TEST BETWEEN CHANNEL 1 AND 2
MOV #1,CH1
MOV #2,CH2
MOV CH2,CHANL 1S:
JSR PC,GETEDG ;GET EDGE VALUES
CLR R2
JSR PC,SET1A ;SCALING = .02 LSB
JSR PC,SET1A ;MAKE IT .01 LSB
BPL 2S
NEG R2 ;MAKE IT POSITIVE
MOV R2,R4 2S:
MOV #1,EDGFLG
JSR PC,TYPSET ;TYPE SETTLING INFORMATION
CMP #2,CH1 ;DONE?
BEQ TST37 ;YES
MOV CH1,R2 ;SETTLE THE OTHER WAY
MOV CH2,CH1
MOV R2,CH2
BR 1S ;
3S:
*****
*TEST 37 DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST
*****
TST37: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
TST $PASS ;FIRST TIME-SKIP DIFLIN
BEQ LEND
JSR PC,DIFLIN
LEND: RTS ;RETURN TO TEST SECTION
OFFSET: MOV #4001,EDGE ;4000,4001 EDGE
JSR RS,SARSUB
SO.
MOV DAC,TEMP
MOV #4000,EDGE ;3777,4000 EDGE
JSR RS,SARSUB
SO.
ADD DAC,TEMP
SUB #400,TEMP
RTS PC

```

J03

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DVADAA.CMB T37

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DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST

1441	005234	013702	001356		TOFF:	MOV	TEMP,R2		
1442	005240	100402				BMI	1\$:: IS THE NUMBER POSITIVE?
1443	005242	104400	012545			TYPE	,POSITV		
1444	005246	104412			1\$:	TYPDC			
1445	005250	104400	013751			TYPE	MLSB		;TYPE ASCIZ STRING
1446	005254	000207				RTS	PC		
1447	005256	005303			TCHK:	DEC	R3		; DECREMENT COUNT
1448	005260	001005				BNE	1\$		
1449	005262	012703	000005			MOV	#5,R3		; RESET COUNT
1450	005266	104400	001171			TYPE	,SCLF		; TYPE A CARRIAGE RETURN AND LINE FEED
1451	005272	000402				BR	2\$		
1452	005274	104400	012114		1\$:	TYPE	,SPACE		; TYPE FOUR (4) SPACES
1453	005300	005037	001416		2\$:	CLR	DELAY		; CLEAR DELAY
1454	005304	005077	173634			CLR	@STKS		; CLEAR INTERRUPT ENABLE
1455	005310	105777	173630		3\$:	TSTB	@STKS		; IS KEYBOARD FLAG SET?
1456	005314	100404				BMI	4\$; YES
1457	005316	005237	001416			INC	DELAY		; IS DELAY ZERO?
1458	005322	001372				BNE	3\$; NO
1459	005324	000416				BR	6\$		
1460	005326	005777	173614		4\$:	TST	@STKB		; CLEAR FLAG
1461	005332	012777	000100	173604		MOV	#100,@STKS		; SET INTERRUPT ENABLE
1462	005340	004537	011130			JSR	RS,COMPAR		; TEST LAST CONVERSION
1463	005344	000000				D			
1464	005346	011574				V4			; TOLERANCE .04 LSB
1465	005350	000402				BR	5\$		
1466	005352	062716	000002			ADD	#2,(SP)		; BUMP RETURN ADDRESS
1467	005356	062716	000002		5\$:	ADD	#2,(SP)		; BUMP RETURN ADDRESS 2 WORDS
1468	005362	000207			6\$:	RTS	PC		
1469	005364	104400	012236		BEGINC:	TYPE	,CCHAN		; ASK FOR CHANNEL
1470	005370	104407				RDOCT			; READ CHANNEL NUMBER
1471	005372	012637	001372			MOV	(SP)+,CHANL		; STORE CHANNEL NUMBER
1472	005376	013737	001372	001370		MOV	CHANL,DUMMY		; LOAD DUMMY
1473	005404	104400	012264		1\$:	TYPE	,SEL		; SELECT OFFSET OR GAIN ADJUST
1474	005410	104406				ROLIN			; GET TEST
1475	005412	012600				MOV	(SP)+,RO		; MOVE POINTER TO RO
1476	005414	121027	000117			CMPE	(RO),#'0		; IS IT "0"?
1477	005420	001406				BEQ	AJOFF		; YES, GO TO ADJUST OFFSET
1478	005422	121027	000107			CMPE	(RO),#'G		; IS IT "G"?
1479	005426	001430				BEQ	AJGAIN		; YES, GO TO ADJUST GAIN
1480	005430	104400	001170			TYPE	,SQUES		; TYPE "?"
1481	005434	000763				BR	1\$::

K03

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DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST

```

1482 005436 104400 012377      AJOFF:  TYPE      ,IGND      ;GROUND CHANNEL
1483 005442 104406      RDLIN      ;WAIT FOR CR
1484 005444 005726      TST        (SP)+    ;POP 1 WORD OFF STACK
1485 005446 104400 012337      1$:      TYPE      ,XADJ      ;ADJUST MESSAGE
1486 005452 104400 012436      TYPE      ,CRWR      ;TYPE "TYPE CR WHEN READY"
1487 005456 012703 000005      MOV        #5,R3    ;SET UP COUNT
1488 005462 004737 005150      2$:      JSR        PC,OFFSET ;TEST AND TYPE OFFSET ERROR
1489 005466 004737 005274      JSR        PC,TOFF   ;TYPE OFFSET
1490 005472 004737 005256      JSR        PC,TCHK   ;CHECK FOR A CHARACTER AND DELAY
1491 005476 000771      BR         2$       ;
1492 005500 000762      BR         1$       ;;NOT WITHIN TOLLERANCE, TRY AGAIN
1493 005502 000005      RESET
1494 005504 000137 002262      JMP        BEG2
1495 005510 104400 012465      AJGAIN:  TYPE      ,IVOLT     ;INPUT +5.115 VOLTS ON CHANNEL
1496 005514 104400 012436      TYPE      ,CRWR
1497 005520 104406      RDLIN      ;WAIT FOR CR
1498 005522 005726      TST        (SP)+    ;POP 1 WORD OFF STACK
1499 005524 104400 012531      1$:      TYPE      ,YADJ      ;ADJUST MESSAGE
1500 005530 104400 012353      TYPE      ,MOLSB    ;TYPE " " FOR 0.00 LSB ERROR"
1501 005534 104400 012436      TYPE      ,CRWR
1502 005540 012703 000005      MOV        #5,R3    ;SET UP COUNT
1503 005544 012737 007777 001420  2$:      MOV        #7777,EDGE ;LOOK FOR 7776,7777 EDGE
1504 005552 004537 007074      JSR        RS,SARSUB
1505 005556 000052      SO.
1506 005560 013737 001414 001356      MOV        DAC,TEMP  ;SAVE DAC
1507 005566 012737 007776 001420      MOV        #7776,EDGE ;LOOK FOR 7775,7776 EDGE
1508 005574 004537 007074      JSR        RS,SARSUB
1509 005580 000052      SO.
1510 005602 063737 001414 001356      ADD        DAC,TEMP  ;ADD RESULTS
1511 005610 162737 000400 001356      SUB        #400,TEMP  ;OFFSET RESULT
1512 005616 004737 005234      JSR        PC,TOFF   ;TYPE GAIN
1513 005622 004737 005256      JSR        PC,TCHK   ;CHECK FOR CHARACTER AND DELAY
1514 005626 000746      BR         2$       ;
1515 005630 000735      BR         1$       ;;NOT WITHIN TOLLERANCE, TRY AGAIN
1516 005632 000005      RESET
1517 005634 000137 002262      JMP        BEG2

```

```

1518          .SBTTL          PRINT VALUES ROUTINE
1519 005640 012737 005640 001374 BEGINP: MOV      #BEGINP,TADDR      ;TEST ADDRESS IN TADDR
1520 005646 005077 173444          CLR      @STREG          ;CLEAR STATUS REGISTER
1521 005652 104400 013645          TYPE     ,HEADS          ;TYPE OUT HEADING
1522 005656 005046          CLR      -(SP)          ;CLEAR PSW
1523 005660 012746 005666          MOV      #1$,-(SP)
1524 005664 000002          RTI
1525 005666 017700 173246          1$: MOV      @SWR,R0          ;READ CHANNEL FROM SWITCH REG.
1526 005672 042700 177700          BIC      #177700,R0      ;ISOLATE MUX BITS
1527 005676 032777 020000 173234 BIT      @BIT13,@SWR      ;IS BIT 13 SET?
1528 005704 001005          BNE      2$              ;;YES,SKIP TYPEOUT
1529 005706 104400 012111          TYPE     CH
1530 005712 010046          MOV      R0,-(SP)        ;SAVE R0 FOR TYPEOUT
1531          ;TYPE CHANNEL
1532 005714 104402          TYPOS    2              ;GO TYPE--OCTAL ASCII
1533 005716          .BYTE    2              ;TYPE 2 DIGIT(S)
1534 005717          .BYTE    0              ;SUPPRESS LEADING ZEROS
1535 005720 012777 001620 173376 2$: MOV      @RETURN,@VECTOR ;ADDRESS AFTER INTRPT.
1536 005726 000300          SWAB     R0              ;SWITCH BYTES
1537 005730 052700 000100          BIS      @BIT6,R0
1538 005734 010077 173356          MOV      R0,@STREG      ;LOAD THE CHANNEL
1539 005740 012702 000010          MOV      #10,R2         ;TYPEOUT COUNTER
1540 005744 005277 173346          3$: INC      @STREG      ;START CONVERSION
1541 005750 000001          WAIT
1542 005752 017700 173344          MOV      @A0BUFF,R0     ;READ CONVERTED VALUE
1543 005756 032777 020000 173154 BIT      @BIT13,@SWR      ;IS BIT 13 SET?
1544 005764 001403          BEQ      4$              ;NOT SET, TYPE OUT LIST
1545 005766 010077 173150          MOV      R0,@DISPLAY    ;PUT VALUE IN DISPLAY FOR DISPLAY CONTR
1546 005772 000735          BR       1$              ;REPEAT CONVERSION
1547 005774 104400 012114          4$: TYPE     SPACE
1548 006000 010046          MOV      R0,-(SP)        ;SAVE R0 FOR TYPEOUT
1549          ;PRINT OCTAL CONVERTED VALUE
1550 006002 104402          TYPOS    4              ;GO TYPE--OCTAL ASCII
1551 006004          .BYTE    4              ;TYPE 4 DIGIT(S)
1552 006005          .BYTE    1              ;TYPE LEADING ZEROS
1553 006006 012701 010000          MOV      #10000,R1
1554 006012 005301          5$: DEC      R1
1555 006014 001376          BNE      5$
1556 006016 005302          DEC      R2              ;DECREMENT THE COUNTER
1557 006020 001351          BNE      3$              ;NO CARRIAGE RETURN
1558 006022 104400 001171          TYPE     $CRLF          ;CARRIAGE RETURN
1559 006026 000717          BR       1$              ;REPEAT CONVERSION

```


1560					.SBTTL	LOGIC TEST SECTION	
1561	006030	012737	006030	001374	BEG1:	MOV	#BEG1, TADDR
1562	006036	004737	002464			JSR	PC, TESTAD
1563	006042	004737	002670		1\$	JSR	PC, BEGINL
1564	006046	004737	006206			JSR	PC, BUMPAD
1565	006052	000773				BR	1\$
1566	006054	012737	006042	011626		MOV	#1\$, AGTST
1567	006062	000137	011630			JMP	SEOP
1568							
1569					.SBTTL	AUTO TEST	
1570	006066	012737	006066	001374	BEGIN1A:	MOV	#BEGIN1A, TADDR
1571	006074	004737	002464			JSR	PC, TESTAD
1572	006100	004737	002670		1\$:	JSR	PC, BEGINL
1573	006104	104400	013037			TYPE	MEND
1574	006110	013746	001316			MOV	\$STREG, -(SP)
1575	006114	104402				TYPOS	
1576	006116	006				.BYTE	6
1577	006117	001				.BYTE	1
1578	006120	104400	001171			TYPE	\$SCRLF
1579	006124	004737	004126			JSR	PC, WRAP
1580	006130	004737	006206			JSR	PC, BUMPAD
1581	006134	000761				BR	1\$
1582	006136	012737	006100	011626		MOV	#1\$, AGTST
1583	006144	000137	011630			JMP	SEOP
1584							
1585					.SBTTL	WRAPAROUND TEST	
1586	006150	012737	006150	001374	BEGIN1W:	MOV	#BEGIN1W, TADDR
1587	006156	004737	002464			JSR	PC, TESTAD
1588	006162	004737	004126		1\$:	JSR	PC, WRAP
1589	006166	004737	006206			JSR	PC, BUMPAD
1590	006172	000773				BR	1\$
1591	006174	012737	006162	011626		MOV	#1\$, AGTST
1592	006202	000137	011630			JMP	SEOP

```

;TEST ADDRESS
;NO OF ADDITIONAL AD'S
;LOGIC TESTS
;MORE TO TEST?
;TEST NEXT A/D
;ADDRESS FOR EOP
;TYPE END OF PASS

```

```

;TEST ADDRESS
;NO. OF AD'S TO BE TESTED
;LOGIC TESTS
;TYPE END OF LOGIC TEST
;SAVE STREG FOR TYPEOUT
;TYPE OCTAL NUMBER
;TYPE 6 DIGITS
;TYPE LEADING ZEROS
;TYPE A CR, LF

```

```

;TEST NEXT A/D
;TEST NEXT AD
;ADDRESS FOR EOP
;TYPE END OF PASS

```

```

;TEST ADDRESS
;NO. OF AD'S TO BE TESTED
;WRAPAROUND TESTS
;MORE A/D'S TO BE TESTED?
;YES-GO TEST NEXT ADV11

```

```

;INCREMENTS $PASS

```

1593					.SBTTL	DETERMINE IF MORE ADV11'S TO BE TESTED	
1594	006206	005737	001364		BUMPAD: TST	NBEXT	;ADDITIONAL AD'S?
1595	006212	001434			BEQ	FIXADR	;NO-INITIALIZE ADDRESSES
1596	006214	006337	001440		ASL	TSTBIT	;MOVE BIT TO NEXT MODULE
1597	006220	063737	001336	001316	ADD	VADR,STREG	;SET UP NEW ST. REG.
1598	006226	063737	001336	001320	ADD	VADR,AOST1	;SET UP NEW AOST1
1599	006234	063737	001336	001322	ADD	VADR,ABUFF	;SET UP NEW BUFFER ADDRESS
1600	006242	063737	001340	001324	ADD	VVCT,VECTOR	;SET UP NEW VECTOR
1601	006250	063737	001340	001330	ADD	VVCT,VECTR1	
1602	006256	063737	001340	001332	ADD	VVCT,VECTR2	
1603	006264	063737	001340	001334	ADD	VVCT,VECTR3	
1604	006272	005077	173032		CLR	AVECTR1	
1605	006276	005337	001364		DEC	NBXT	;ONE LESS ADV11
1606	006302	000465			BR	BY ASS	
1607	006304	062716	000002		FIXADR: ADD	#2,(SP)	
1608	006310	012737	000001	001440	FIXONE: MOV	#1,TSTBIT	;INITIALIZE MODULE ERROR TEST BIT
1609	006316	013737	001250	001316	MOV	\$BASE,STREG	;RELOAD INITIAL ADDRESSES
1610	006324	013737	001250	001320	MOV	\$BASE,AOST1	
1611	006332	013737	001250	001322	MOV	\$BASE,ABUFF	
1612	006340	005237	001320		INC	AOST1	
1613	006344	062737	000002	001322	ADD	#2,ABUFF	
1614	006352	013737	001244	001324	MOV	\$VECT1,VECTOR	
1615	006360	042737	170000	001324	BIC	#170000,VECTOR	
1616	006366	113737	001245	001326	MOVB	\$VECT1+1,BASEBR	
1617	006374	105037	001327		CLRB	BASEBR+1	;CLEAR HIGH BYTE
1618	006400	013737	001324	001330	MOV	VECTOR,VECTR1	
1619	006406	062737	000002	001330	ADD	#2,VECTR1	
1620	006414	013737	001324	001332	MOV	VECTOR,VECTR2	
1621	006422	062737	000004	001332	ADD	#4,VECTR2	
1622	006430	013737	001324	001334	MOV	VECTOR,VECTR3	
1623	006436	062737	000006	001334	ADD	#6,VECTR3	
1624	006444	005077	172660		CLR	AVECTR1	
1625	006450	013737	001366	001364	MOV	NMBEXT,NBEXT	;RESET COUNTER
1626					::LOAD	+.2 AND HALT TRAP CATCH;;	
1627	006456	012700	000216		BYPASS: MOV	#216,R0	;FILL .+2
1628	006462	012701	000214		MOV	#214,R1	;LOAD HALT
1629	006466	020137	001344		1\$: CMP	R1,KBVECT	
1630	006472	001410			BEQ	2\$	
1631	006474	010021			MOV	R0,(R1)+	
1632	006476	005021			CLR	(R1)+	
1633	006500	010100			MOV	R1,R0	
1634	006502	005720			TST	(R0)+	
1635	006504	020027	001002		CMP	R0,#1002	
1636	006510	001366			BNE	1\$	
1637	006512	000207			RTS	PC	;TEST NEXT A/D
1638	006514	022021			2\$: CMP	(R0)+,(R1)+	
1639	006516	022021			CMP	(R0)+,(R1)+	
1640	006520	000762			BR	1\$	

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1641 006522 104412          TYPSET: TYPDC
1642 006524 104400 012121  TYPE      ,LSB
1643 006530 013746 001352  MOV      CH2,-(SP)      ;; SAVE CH2 FOR TYPEOUT
1644                                     ;; TYPE CH
1645 006534 104402          TYPOS
1646 006536          002      .BYTE     2      ;; GO TYPE--OCTAL ASCII
1647 006537          000      .BYTE     0      ;; TYPE 2 DIGIT(S)
1648 006540 104400 013757  TYPE      MAT      ;; SUPPRESS LEADING ZEROS
1649 006544 004737 007030  JSR      PC,TYPEDG    ;; TYPE ASCII STRING
1650 006550 104400 012134  TYPE      SETCH
1651 006554 013746 001360  MOV      CH1,-(SP)      ;; SAVE CH1 FOR TYPEOUT
1652                                     ;; TYPE CH
1653 006560 104402          TYPOS
1654 006562          002      .BYTE     2      ;; GO TYPE--OCTAL ASCII
1655 006563          000      .BYTE     0      ;; TYPE 2 DIGIT(S)
1656 006564 104400 012156  TYPE      ATMSG      ;; SUPPRESS LEADING ZEROS
1657 006570 013737 001360 006616  MOV      CH,15
1658 006576 163737 001342 006616  SUB      BASE14,15
1659 006604 012777 000200 172510  MOV      #200,ADBUFF
1660 006612 004537 011016  JSR      R5,CONVRT
1661 006616 000000          IS:      0
1662 006620 013746 001356  MOV      TEMP,-(SP)      ;; SAVE TEMP FOR TYPEOUT
1663                                     ;; TYPE VALUE
1664 006624 104402          TYPOS
1665 006626          004      .BYTE     4      ;; GO TYPE--OCTAL ASCII
1666 006627          001      .BYTE     1      ;; TYPE 4 DIGIT(S)
1667 006630 020437 011610  CMP      R4,VSET
1668 006634 003003          BGT      ERR
1669 006636 104400 012225  TYPE      ,OKMSG
1670 006642 000207          RTS      PC
1671 006644 104400 012547  ERR:    TYPE      ,ERMSG
1672 006650 000207          RTS      PC
1673
1674                                     ;; SUBROUTINE FOR SETTLING TESTS;;
1675 006652 013737 001362 001370  SETIA:  MOV      CH2,DUMMY      ;LOAD DUMMY
1676 006660 004537 007074          JSR      R5,SARSUB      ;DO SAR ROUTINE AT 50%
1677 006664 000062          SO.
1678 006666 063702 001414          ADD      DAC,R2      ;ADD RESULT TO R2
1679 006672 013737 001360 001370  MOV      CH1,DUMMY      ;CHANGE DUMMY VALUE
1680 006700 004537 007074          JSR      R5,SARSUB      ;DO SAR ROUTINE AT 50%
1681 006704 000062          SO.
1682 006706 163702 001414          SUB      DAC,R2      ;SUBTRACT RESULT FROM R2
1683 006712 000207          RTS      PC      ;RETURN

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1684                                     ; SUBROUTINE TO GET EDGE VALUE
1685                                     ; CALL=JSR PC,GETEDG
1686                                     ; CONVERSIONS ON A/D CHANNEL 'CHANL'
1687                                     ; RESULT IN EDGE, USES R0
1688 006714 012777 000200 172400 GETEDG: MOV     #200, @A0BUFF ; LOAD VERNIER DAC
1689 006722 113700 001372          MOVB    CHANL, R0      ; GET CHANNEL
1690 006726 000300          SWAB    R0              ; SET UP A.D STATUS REG.
1691 006730 052700 000100          BIS     #100, R0      ; ENABLE INTRPT.
1692 006734 010077 172356          MOV     R0, @STREG
1693 006740 012700 000100          MOV     #100, R0      ; DAC SETTLING DELAY
1694 006744 005300          IS:    DEC     R0
1695 006746 001376          BNE    IS
1696 006750 005037 001420          CLR     EDGE
1697 006754 012700 000010          MOV     #10, R0
1698 006760 012777 001620 172336          MOV     @RETURN, @VECTOR ; RETURN ADDRESS
1699 006766 005277 172324          CONV: INC     @STREG    ; START CONVERSION
1700 006772 000001          WAIT   ; WAIT FOR INTERRUPT
1701 006774 067737 172322 001420          ADD     @A0BUFF, EDGE
1702 007002 005300          DEC     R0
1703 007004 001370          BNE    CONV
1704 007006 006237 001420          ASR    EDGE
1705 007012 006237 001420          ASR    EDGE
1706 007016 006237 001420          ASR    EDGE
1707 007022 005537 001420          ADC     EDGE
1708 007026 000207          RTS     PC
1709
1710                                     ; SUBROUTINE TO TYPE EDGE VALUES;;
1711 007030 013703 001420          TYPEDG: MOV    EDGE, R3
1712 007034 010346          MOV    R3, -(SP) ; SAVE R3 FOR TYPEOUT
1713                                     ; TYPE OCTAL VALUE OF EDGE
1714 007036 104402          TYPPOS ; GO TYPE--OCTAL ASCII
1715 007040 004          .BYTE 4 ; TYPE 4 DIGIT(S)
1716 007041 001          .BYTE 1 ; TYPE LEADING ZEROS
1717 007042 023727 007072 000001          CMP    EDGFLG, #1
1718 007050 001407          BEQ    RET
1719 007052 062703 000007          ADD    #7, R3
1720 007056 104400 012025          TYPE  , MINUS ; TYPE ASCII STRING
1721 007062 010346          MOV    R3, -(SP) ; SAVE R3 FOR TYPEOUT
1722                                     ; TYPE EDGE VALUE
1723 007064 104402          TYPPOS ; GO TYPE--OCTAL ASCII
1724 007066 004          .BYTE 4 ; TYPE 4 DIGIT(S)
1725 007067 001          .BYTE 1 ; TYPE LEADING ZEROS
1726 007070 000207          RET:    RTS     PC
1727 007072 000600          EDGFLG: 0

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1728 ;SUBROUTINE TO DO SUCCESSIVE APPROXIMATION ROUTINE
1729 ;CALL=JSR R5,SARSUB
1730 ;XXX·XXX=PERCENT
1731 ;RESULT RETURNED IN 'DAC' USES R0,R1,R4
1732 007074 012537 001432 SARSUB: MOV (R5)+,PERCNT ;GET PERCENT
1733 007100 006337 001432 ASL PERCNT
1734 007104 006337 001432 ASL PERCNT
1735 007110 006337 001432 ASL PERCNT ;RESCALE PERCENT FOR 1600.
1736 007114 006337 001432 ASL PERCNT ;POINTS PER BURST
1737 007120 012737 000200 001422 SAR1: MOV #200,BITPNT ;INITIALIZE BIT POINTER AT MSB
1738 007126 005037 001414 CLR DAC ;INITIALIZE DAC VALUE
1739 007132 005000 TRY: CLR R0
1740 007134 063737 001422 001414 R00 BITPNT,DAC ;TRY BIT
1741 007142 013777 001414 172152 MOV DAC,2A0BUFF
1742 007150 012701 003100 MOV #1600,R1 ;SET UP FOR 1600. CONVERSIONS
1743 007154 113777 001370 172136 NXTCVT: MOVB DUMMY,2A0ST1 ;PRESET MUX TO DUMMY CHANNEL
1744 007162 012777 001620 172134 MOV #RETURN,2VECTOR ;RETURN ADDRESS
1745 007170 052777 000101 172120 BIS #101,2STREG ;CONVERSION ON DUMMY CHANNEL
1746 007176 000001 WAIT ;WAIT FOR INTERRUPT
1747 007200 017704 172116 MOV 2A0BUFF,R4 ;DUMMY READ
1748 007204 013704 001372 MOV CHANL,R4
1749 007210 000304 SHAB R4
1750 007212 052704 000101 BIS #101,R4 ;INTERRUPT ENABLE START
1751 007216 010477 172074 MOV R4,2STREG ;JUMP TO CHANNEL + START CONVERT
1752 007222 000001 WAIT ;WAIT FOR INTERRUPT
1753 007224 027737 172072 001420 CMP 2A0BUFF,EDGE
1754 007232 002001 BGE ZS
1755 007234 005200 INC R0 ;COUNT RESULTS .LT. EDGE
1756 007236 005301 ZS: DEC R1
1757 007240 001345 BNE NXTCVT
1758 007242 020037 001432 CMP R0,PERCNT
1759 007246 003003 BGT SHIFT
1760 007250 163737 001422 001414 SUB BITPNT,DAC ;TAKE THE BIT OUT
1761 007256 006237 001422 SHIFT: ASR BITPNT
1762 007262 001323 BNE TRY
1763 007264 000205 RTS RS

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E04

MAINDEC-11-DVADA-A
DVADAA.CMB

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DETERMINE IF MORE ADV11'S TO BE TESTED

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1764      ::DIFFERENTIAL LINEARITY SUBROUTINE;;
1765 007266 104400 013162 01FLIN: TYPE MSG20
1766 007272 013702 001376      MOV RMA,R2      ;SET UP RANDOM NUMBER GENERATOR
1767 007276 013704 001400      MOV RMB,R4
1768 007302 013705 001402      MOV RMC,R5
1769 007306 012700 017754      MOV #BUFFER,R0
1770 007312 012701 010000      MOV #4096.,R1      ;4096 WORDS FOR HISTOGRAM
1771 007316 005020 CLEAR1: CLR (R0)+      ;CLEAR BUFFER AREA
1772 007320 005301      DEC R1
1773 007322 001375      BNE CLEAR1
1774 007324 012700 017134      MOV #DIST,R0      ;DISTRIBUTION BUFFER POINTER
1775 007330 012701 000310      MOV #200.,R1      ;200. WORDS FOR DISTRIBUTION
1776 007334 005003      CLR R3
1777 007336 005037 001434      CLR OUT
1778 007342 005037 001346      CLR WIDE
1779 007346 005037 001350      CLR NARROW
1780 007352 005037 001352      CLR FIRST
1781 007356 005037 001354      CLR SKIPST
1782 007362 005020 CLEAR2: CLR (R0)+      ;CLEAR DISTRIBUTION BUFFER AREA
1783 007364 005301      DEC R1
1784 007366 001375      BNE CLEAR2
1785 007370 012700 000003 CHANNL: MOV #3,R0      ;CHANNEL 3
1786 007374 063700 001342      ADD BASECH,R0
1787 007400 000300      SWAB R0      ;LOAD MUX BITS
1788 007402 052700 000100      BIS #100,R0
1789 007406 010077 171704      MOV R0,2STREG
1790 007412 012700 001440      MOV #800.,R0      ;NOMINAL STATE WIDTH - 1 LSB
1791 007416 012777 001620 171700      MOV #RETURN,2VECTOR
1792 007424 012701 007776 AGAIN: MOV #4094.,R1
1793 007430 060402 NEXT: ADD R4,R2
1794 007432 060502      ADD R5,R2
1795 007434 005502      ADC R2,R2
1796 007436 060204      ADD R3,R4
1797 007440 060504      ADD R5,R4
1798 007442 005504      ADC R4,R4
1799 007444 060205      ADD R2,R5
1800 007446 060405      ADD R4,R5
1801 007450 005505      ADC R5,R5
1802 007452 010246      MOV R2,-(SP)
1803 007454 042702 177760      BIC #177760,R2      ;MASK IT TO 4 BITS ONLY
1804 007460 001402      BEQ CONVR
1805 007462 005302 DELAY3: DEC R2      ;STALL
1806 007464 001376      BNE DELAY3      ;TIME
1807 007466 005277 171624 CONVR: INC 2STREG      ;START CONVERSION
1808 007472 000001      WAIT
1809 007474 017702 171622      MOV 2ADBUFF,R2      ;GET CONVERTED VALUE
1810 007500 001413      BEQ DELAY1      ;IGNORE IF =0
1811 007502 020227 007777      CMP R2,#7777      ;IGNORE IF =7777
1812 007506 001413      BEQ DELAY2
1813 007510 006302      ASL R2
1814 007512 005262 017754      INC BUFFER(R2)      ;MAKE HISTOGRAM
1815 007516 100013      BPL OKAY
1816 007520 012762 077777 017754      MOV #077777,BUFFER(R2)      ;PREVENT OVERFLOW
1817 007526 000407      BR OKAY

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1818	007530	020227	007777	DELAY1:	CMP	R2, #7777			
1819	007534	001400			BEQ	DELAY2			; EQUALIZE LOOP TIME
1820	007536	005201		DELAY2:	INC	R1			; WITH DUMMY INSTR.
1821	007540	005263	001356		INC	TEMP(R3)			
1822	007544	100404			BMI	NOTOK			
1823	007546	012602		OKAY:	MOV	(SP)+, R2			; POP RANDOM NUMBER FROM STACK
1824	007550	005301			DEC	R1			
1825	007552	001326			BNE	NEXT			
1825	007554	000403			BR	AROUND			
1827	007556	005037	001356	NOTOK:	CLR	TEMP			
1828	007562	000771			BR	OKAY			
1829	007564	005300		AROUND:	DEC	R0			
1830	007566	001316			BNE	AGAIN			
1831	007570	012700	007776		MOV	#4094, R0			
1832	007574	012701	017756		MOV	#BUFFER+2, R1			
1833	007600	012102		READ:	MOV	(R1)+, R2			; GET STATE WIDTH
1834	007602	006202			RSR	R2			; 1 LSB = 800.
1835	007604	006202			RSR	R2			
1836	007606	006202			RSR	R2			
1837	007610	005502			RDC	R2			; 1 LSB = 100.
1838	007612	020227	000310		CMP	R2, #200.			; OUT OF RANGE?
1839	007616	002403			BLT	INRNGE			
1840	007620	005237	001434		INC	OUT			; YES - INCREMENT COUNTER
1841	007624	000423			BR	TYPBAD			
1842	007626	006302		INRNGE:	ASL	R2			
1843	007630	005262	017134		INC	DIST(R2)			; MAKE STATE WIDTH DISTRIBUTION
1844	007634	006202			RSR	R2			
1845	007636	020227	000062		CMP	R2, #50.			; IS IT 1/2 LSB?
1846	007642	002007			BGE	NOTNAR			
1847	007644	005237	001350		INC	NARROW			
1848	007650	005702			TST	R2			; IS IT A SKIPPED STATE?
1849	007652	001002			BNE	31\$			
1850	007654	005237	001354		INC	SKIPST			
1851	007660	000405		31\$:	BR	TYPBAD			
1852	007662	020227	000226	NOTNAR:	CMP	R2, #150.			; IS IT 1.5 LSB?
1853	007666	003425			BLE	LAST			
1854	007670	005237	001346		INC	WIDE			
1855	007674	005737	001352	TYPBAD:	TST	FIRST			
1856	007700	001004			BNE	60\$			
1857	007702	005237	001352		INC	FIRST			
1858	007706	104400	012071		TYPE	, STATE			
1859	007712	010103		60\$:	MOV	R1, R3			
1860	007714	162703	017756		SUB	#BUFFER+2, R3			
1861	007720	006203			RSR	R3			
1862	007722	010346			MOV	R3, -(SP)			:: SAVE R3 FOR TYPEOUT
1863									:: TYPE STATE
1864	007724	104402		TYPOS					:: GO TYPE--OCTAL ASCII
1865	007726	004		.BYTE	4				:: TYPE 4 DIGIT(S)
1866	007727	001		.BYTE	1				:: TYPE LEADING ZEROS
1867	007730	104400	012065	TYPE	, DASH				
1868	007734	104412		TYPDC					
1869	007736	104400	012056	TYPE	, LSBMSG				

1870	007742	005300		LAST:	DEC	R0		
1871	007744	001315			BNE	READ		
1872	007746	112737	000177	014554	MOVB	#177,DECPNT		
1873	007754	013702	001354		MOV	SKIPST,R2		;GET NO. OF SKIPPED STATES
1874	007760	104412			TYPDC			;TYPE IT
1875	007762	104400	012564		TYPE	SKPMSG		;TYPE MESSAGE
1876	007766	005737	001354		TST	SKIPST		
1877	007772	001403			BEQ	IS		
1878	007774	104400	012547		TYPE	ERMSG		;TYPE "ERROR"
1879	010000	000402			BR	NAR		
1880	010002	104400	012225	IS:	TYPE	OKMSG		;TYPE #OK#
1881	010006	013702	001350	NAR:	MOV	NARROW,R2		;GET NO. OF NARROW STATES
1882	010012	104412			TYPDC			;TYPE IT
1883	010014	104400	012606		TYPE	NARMSG		;TYPE MESSAGE
1884	010020	013702	001346		MOV	WIDE,R2		
1885	010024	063702	001434		ADD	OUT,R2		
1886	010030	104412			TYPDC			;TYPE NO. OF WIDE STATES
1887	010032	104400	012645		TYPE	WIDMSG		;TYPE MESSAGE
1888	010036	013702	001434		MOV	OUT,R2		
1889	010042	104412			TYPDC			;TYPE NO. OF STATES OUTSIDE 2 LSB
1890	010044	104400	012704		TYPE	OUTMSG		;TYPE MESSAGE
1891	010050	005737	001434		TST	OUT		
1892	010054	001403			BEQ	IS		
1893	010056	104400	012547		TYPE	ERMSG		;TYPE "ERROR"
1894	010062	000402			BR	HALF		
1895	010064	104400	012225	11S:	TYPE	OKMSG		;TYPE "OK"
1896	010070	013702	001350	HALF:	MOV	NARROW,R2		
1897	010074	063702	001346		ADD	WIDE,R2		
1898	010100	063702	001434		ADD	OUT,R2		
1899	010104	010200			MOV	R2,R0		
1900	010106	104412			TYPDC			;TYPE NO. OF STATES OUTSIDE LIMITS
1901	010110	112737	000056	014554	MOVB	#56,DECPNT		
1902	010116	104400	012737		TYPE	HAFMSG		
1903	010122	020027	000051		CHP	R0,#41.		;COMPARE IT TO NOMINAL
1904	010126	003403			BLE	21\$		
1905	010130	104400	012547		TYPE	ERMSG		;TYPE "ERROR"
1906	010134	000402			BR	SWDIST		
1907	010136	104400	012225	21S:	TYPE	OKMSG		;TYPE "OK"
1908	010142	005737	001410	SWDIST:	TST	FLAG		;VT55?
1909	010146	001426			BEQ	RELACC		
1910	010150	004737	010626		JSR	PC,DELCLR		;WAIT AWHILE, THEN CLEAR VT55
1911	010154	104400	013214		TYPE	MSG16		
1912	010160	104400	014006		TYPE	BUFF1		;TYPE BUFF1-PRINT GRID
1913	010164	012700	017134		MOV	WDIST,R0		;POINTER TO STATE WIDTH DISTRIBUTION
1914	010170	012701	000310		MOV	#200.,R1		;GO 200. TIMES UP TO 2 LSB
1915	010174	012002		NXTY1:	MOV	(R0)+,R2		
1916	010176	004737	011322		JSR	PC,LOADY		
1917	010202	005002			CLR	R2		
1918	010204	004737	011322		JSR	PC,LOADY		
1919	010210	005301			DEC	R1		
1920	010212	001370			BNE	NXTY1		
1921	010214	104400	013727		TYPE	C2		;TYPE ASCIZ STRING
1922	010220	004737	010626		JSR	PC,DELCLR		

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1923 ;CHANGE HISTOGRAM ERROR TO RELATIVE ACCURACY ERROR
1924
1925 010224 005001 RELACC: CLR R1 ;RUNNING ERROR = 0
1926 010226 003003 CLR R3 ;MAXIMUM ERROR = 0
1927 010230 104400 013577 TYPE ,MSG21
1928 010234 012700 017756 MOV #BUFFER+2,R0
1929 010240 011002 NXTSTA: MOV (R0),R2 ;STATE WIDTH = R2
1930 010242 162702 001440 SUB #800.,R2 ;STATE WIDTH ERROR IN R2
1931 010246 060201 ADD R2,R1 ;UPDATE RUNNING ERROR
1932 010250 010120 MOV R1,(R0)+ ;SAVE IN BUFFER
1933 010252 010104 MOV R1,R4 ;SAVE IN R4 ALSO
1934 010254 100001 BPL PLUS ;IS IT POSITIVE?
1935 010256 005404 NEG R4 ;NO - MAKE IT POSITIVE
1936 010260 020403 PLUS: CMP R4,R3 ;CHECK AGAINST PREVIOUS MAX. ERROR
1937 010262 003405 BLE NOTNEW ;NOT A NEW MAXIMUM
1938 010264 010403 MOV R4,R3 ;UPDATE MAXIMUM IN R3
1939 010266 010005 MOV R0,R5
1940 010270 162705 017756 SUB #BUFFER+2,R5
1941 010274 006205 ASR R5 ;R5=EDGE VALUE AT MAX. RELACC
1942 010276 020027 037752 NOTNEW: CMP R0,#BUFFER+8190. ;DONE?
1943 010302 001356 BNE NXTSTA ;NO - REPEAT
1944 010304 006203 ASR R3 ;RESCALE FROM 1 LSB = 800. SCALING
1945 010306 006203 ASR R3 ;TO 1 LSB = 100. SCALING
1946 010310 006203 ASR R3
1947 010312 005503 ROC R3
1948 010314 010302 MOV R3,R2
1949 010316 104412 TYPDC
1950 010320 104400 013624 TYPE LINEA
1951 010324 010546 MOV R5,-(SP) ;SAVE R5 FOR TYPEOUT
1952 ;TYPE VALUE
1953 010326 104402 TYPOS ;GO TYPE--OCTAL ASCII
1954 010330 004 .BYTE 4 ;TYPE 4 DIGIT(S)
1955 010331 001 .BYTE 1 ;TYPE LEADING ZEROS
1956 010332 104400 012223 TYPE SLASH ;PRINT '/'
1957 010336 005205 INC R5
1958 010340 010546 MOV R5,-(SP) ;SAVE R5 FOR TYPEOUT
1959 ;TYPE VALUE
1960 010342 104402 TYPOS ;GO TYPE--OCTAL ASCII
1961 010344 004 .BYTE 4 ;TYPE 4 DIGIT(S)
1962 010345 001 .BYTE 1 ;TYPE LEADING ZEROS
1963 010346 020337 011612 CMP R3,VLIN
1964 010352 003403 BLE 41$
1965 010354 104400 012547 TYPE ,ERMSG
1966 010360 000402 BR 42$
1967 010362 104400 012225 41$: TYPE ,OKMSG
1968 010366 005737 001410 42$: TST FLAG ;VT55?
1969 010372 001503 BEQ L02
1970 010374 012700 017754 MOV #BUFFER,R0
1971 010400 012701 010000 MOV #4096.,R1

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1972	010404	011002		GETDAT:	MOV	(R0),R2		;GET RELATIVE ACCURACY ERROR SCALED 1LSB = 800.
1973	010406	006202			ASR	R2		;RESCALE IT TO 1 LSB = 100.
1974	010410	006202			ASR	R2		
1975	010412	006202			ASR	R2		
1976	010414	005502			ADC	R2		
1977	010416	062702	000166		ADD	#118.,R2		;AND MOVE IT TO MID-SCREEN
1978	010422	010220			MOV	R2,(R0)+		;PUT IT BACK INTO BUFFER
1979	010424	005301			DEC	R1		
1980	010426	001366			BNE	GETDAT		
1981	010430	012700	017754		MOV	#BUFFER,R0		
1982	010434	012704	017754		MOV	#BUFFER,R4		
1983	010440	012705	017756		MOV	#BUFFER+2,R5		
1984	010444	012701	001000		MOV	#512.,R1		
1985	010450	012702	000007		NXTB:	MOV	#7.,R2	
1986	010454	012003			MOV	(R0)+,R3		
1987	010456	010337	001424		MOV	R3,MIN		;MINIMUM
1988	010462	010337	001430		MOV	R3,MAX		;MAXIMUM
1989	010466	012003			NXTCMP:	MOV	(R0)+,R3	
1990	010470	020337	001424		CMP	R3,MIN		
1991	010474	002002			BGE	MAXTST		
1992	010476	010337	001424		MOV	R3,MIN		;NEW MINIMUM
1993	010502	020337	001430		MAXTST:	CMP	R3,MAX	
1994	010506	003402			BLE	TSTB		
1995	010510	010337	001430		TSTB:	MOV	R3,MAX	
1996	010514	005302			DEC	R2		;NEW MAXIMUM
1997	010516	001363			BNE	NXTCMP		
1998	010520	013724	001424		MOV	MIN,(R4)+		
1999	010524	013725	001430		MOV	MAX,(R5)+		
2000	010530	022425			CMP	(R4)+,(R5)+		;BUMP EACH ONCE MORE
2001	010532	005301			DEC	R1		
2002	010534	001345			BNE	NXTB		
2003	010536	104400	013122		TYPE	,MSG18		
2004	010542	104400	014034		TYPE	,BUFF2		;TYPE BUFF2
2005	010546	012700	017754		MOV	#BUFFER,R0		
2006	010552	004737	010604		JSR	PC,LOAD		
2007	010556	104400	013734		TYPE	,C3		;TYPE ASCIZ STRING
2008	010562	012700	017756		MOV	#BUFFER+2,R0		
2009	010566	004737	010604		JSR	PC,LOAD		
2010	010572	104400	013727		TYPE	,C2		;TYPE ASCIZ STRING
2011	010576	004737	010626		JSR	PC,DELCLR		
2012	010602	000207			LO2:	RTS		
2013	010604	012701	001000		LOAD:	MOV	#512.,R1	
2014	010610	012002			LOAD0:	MOV	(R0)+,R2	
2015	010612	005720				TST	(R0)+	
2016	010614	004737	011322			JSR	PC,LOADY	
2017	010620	005301				DEC	R1	
2018	010622	001372				BNE	LOAD0	
2019	010624	000207				RTS	PC	

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```

2020 010626 032777 010000 170304 DELCLR: BIT      #BIT12,JSWR      ;TEST FOR HALT FOR DISPLAY
2021 010634 001402          BEQ      18              ;;DON'T HALT FOR DISPLAY
2022 010636 000000          HALT
2023 010640 000407          BR       3S              ;;
2024 010642 005000          1S:    CLR      R0              ;;
2025 010644 012701 000020      MOV     #20,R1          ;DELAY BEFORE CLEANING SCREEN
2026 010650 005300      2S:    DEC     R0
2027 010652 001376          BNE    2S
2028 010654 005301          DEC     R1
2029 010656 001374          BNE    2S
2030 010660 104400 014054      3S:    TYPE    VTINIT
2031 010664 000207          RTS     PC
2032
2033 010666 104400 012163      ;;TYPE RMS AND PEAK VALUES;;
2034 010672 005737 001404      TYPRP: TYPE    NOI
2035 010676 100002          TST     RMS
2036 010700 005037 001404          BPL    POSRMS          ;RMS<0,SET RMS=0
2037 010704 005737 001406      POSRMS: CLR     RMS
2038 010710 100002          TST     PEAK
2039 010712 005037 001406          BPL    POSPEA         ;PEAK<0,SET PEAK=0
2040 010716 013702 001404      POSPEA: CLR     PEAK
2041 010722 104412          MOV     RMS,R2
2042 010724 104400 013006          TYPOC  MESR
2043 010730 013702 001406          TYPE   PEAK,R2      ;TYPE " LSB RMS, "
2044 010734 104412          MOV     PEAK,R2
2045 010736 104400 013021          TYPOC  MESP
2046 010742 004737 007030          TYPE   PC,TYPEDG    ;TYPE " LSB PEAK AT "
2047 010746 104400 012173          JSR    PC,TYPEDG
2048 010752 013746 001372          TYPE   CHAN
2049
2050 010756 104402          MOV     CHAN,-(SP)   ;TYPE " ON CHANNEL "
2051 010760 002          ;;SAVE CHANL FOR TYPEOUT
2052 010761 000          ;;TYPE CHANL
2053 010762 023737 001404 011604      TYPOS  GO TYPE--OCTAL ASCII
2054 010770 003007          .BYTE  2
2055 010772 023737 001406 011606      .BYTE  0
2056 011000 003003          .BYTE  0
2057 011002 104400 012225          CMP    RMS,VNR
2058 011006 000207          BGT    ER
2059 011010 104400 012547          CMP    PEAK,VNP      ;WITHIN LIMITS?
2060 011014 000207          BGT    ER
          TYPE   OKMSG
          RTS   PC
ER:     TYPE   ERMSG
        RTS   PC

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2061      ;;ROUTINE TO AVERAGE 8 CONVERSIONS;;
2062      CONVRT: MOV      (R5)+,R0      ;GET CHANNEL VALUE
2063      011016 012500      001342      ADD      BASECH,R0
2064      011020 063700      001372      MOV      RC,CHANL
2065      011024 010037      SWAB     RO
2066      011030 000300      CLR      TEMP
2067      011032 005037      001356      MOV      RO,@STREG      ;LOAD CHANNEL INTO MIX BITS
2068      011036 010077      170254      MOV      #10000,R0
2069      011042 012700      010000      2$:     DEC      RO
2070      011046 005300      BNE     2$
2071      011050 001376      MOV      #RETURN,@VECTOR      ;LOAD VECTOR
2072      011052 012777      001620 170244  MOV      #10,R0      ;SET UP COUNTER
2073      011060 012700      000010  MOV      #101,@STREG      ;SET INTRPT. EN., START CONV.
2074      011064 152777      000101 170224 1$:     BISB
2075      011072 000001      WAIT
2076      011074 067737      170222 001356  ADD     @A0BUFF,TEMP      ;READ BUFFER
2077      011102 005300      DEC     RO
2078      011104 001367      BNE     1$      ;DO 8 TIMES
2079      011106 006237      001356  ASR     TEMP      ;AVERAGE VALUE
2080      011112 006237      001356  ASR     TEMP
2081      011116 006237      001356  ASR     TEMP
2082      011122 005537      001356  ADC     TEMP
2083      011126 000205      RTS
2084      ;RETURN
2085      ;COMPARE $GDDAT AND $BDDAT:
2086      COMPAR: MOV      (R5)+,$GDDAT      ;GET GOOD DATA
2087      011130 012537      001124  MOV      @R5+,$SPREAD      ;GET SPREAD
2088      011134 013537      001412  MOV      TEMP,$BDDAT      ;GET BAD(ACTUAL) DATA
2089      011140 013737      001356 001126  MOV      $BDDAT,R1
2090      011146 013701      001126  MOV      $GDDAT,R0
2091      011152 013700      001124  SUB     R1,R0      ;GET DIFFERENCE
2092      011156 160100      BPL     7$
2093      011160 100001      NEG     RO
2094      011162 005400      7$:     CMP     RO,SPREAD      ;COMPARE IT TO SPREAD
2095      011164 020037      001412  BGT     10$      ;GO TO ERROR PRINTOUT
2096      011170 003001      BGT     10$
2097      011172 005725      TST     (R5)+
2098      011174 000205      10$:    RTS     R5      ;BUMP RETURN POINTER AROUND ERROR CALL

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2097      ;:SUBROUTINE TO TYPE INTRPT. TST MSG.;;
2098      DUMW:  TST      $PASS
2099      011176 005737 001202      BNE      20$
2100      011202 001021      MOV      #20$, $LPERR
2101      011204 012737 011246 001110  MOV      #20$, $LPADR
2102      011212 012737 011246 001106  TYPE     ME1$T
2103      011220 104400 013764      MOV      RO, -(SP)
2104      011224 010046
2105      011226 104402      TYPOS
2106      011230      002      .BYTE   2
2107      011231      000      .BYTE   0
2108      011232 104400 013063  TYPE     ONAD
2109      011236 013746 001316  MOV      $STREG, -(SP)
2110
2111      011242 104402      TYPOS
2112      011244      006      .BYTE   6
2113      011245      001      .BYTE   1
2114      011246 000207      20$:   RTS      PC
2115
2116      011250 005737 001202      DUMC:  TST      $PASS
2117      011254 001010      BNE      30$
2118      011256 012737 011276 001110  MOV      #30$, $LPERR
2119      011264 012737 011276 001106  MOV      #30$, $LPADR
2120      011272 104400 012210  TYPE     DONE
2121      011276 000207      30$:   RTS      PC

```

```

;:TYPE ASCIZ STRING
;:SAVE RO FOR TYPEOUT
;:TYPE TEST NO.
;:GO TYPE--OCTAL ASCII
;:TYPE 2 DIGIT(S)
;:SUPPRESS LEADING ZEROS
;:SAVE STREG FOR TYPEOUT
;:TYPE BUS ADDRESS
;:GO TYPE--OCTAL ASCII
;:TYPE 6 DIGITS
;:TYPE LEADING ZEROS

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2122          ;SUBROUTINE TO RESET & SET INTRPT. EN.;
2123 011300 000005 RST:  RESET
2124 011302 052777 000100 167634  BIS      #100,2STKS
2125 011310 005046          CLR      -(SP)          ;CLEAR PSW
2126 011312 012746 011320  MOV      #15,-(SP)
2127 011316 000002          RTI
2128 011320 000207  IS:   RTS      PC

2129
2130          ;SUBROUTINE LOADY:
2131 011322 005702 LOADY:  TST      R2          ;ROUTINE TO LOAD VLAUE INTO R2
2132 011324 100001          BPL      PLUSR2        ;AS A VT55 Y-VALUE
2133 011326 005002          CLR      R2
2134 011330 020227 000353  PLUSR2:  CMP      R2,#235.
2135 011334 002402          BLT      LESS
2136 011336 012702 000353  MOV      #235.,R2
2137 011342 010203  LESS:  MOV      R2,R3
2138 011344 042702 177740  BIC      #177740,R2
2139 011350 052702 000040  BIS      #40,R2
2140 011354 105777 167570  B10:   TSTB     2STPS          ;PRINT CHARACTER
2141 011360 100375          BPL      B10
2142 011362 110277 167564  MOVB     R2,2STPB
2143 011366 006203          ASR      R3
2144 011370 006203          ASR      R3
2145 011372 006203          ASR      R3
2146 011374 006203          ASR      R3
2147 011376 006203          ASR      R3
2148 011400 042703 177770  BIC      #177770,R3
2149 011404 052703 000040  BIS      #40,R3
2150 011410 105777 167534  B11:   TSTB     2STPS          ;PRINT CHARACTER
2151 011414 100375          BPL      B11
2152 011416 110377 167530  MOVB     R3,2STPB
2153 011422 000207          RTS      PC

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2154      ;;SUBROUTINE TO TYPE DECIMAL VALUE;;
2155      ;;IN R2 AS X.XX;;
2156      011424 005702      DECTYP: TST      R2          ;TEST VALUE TO BE TYPED
2157      011426 100003      BPL      POS          ;TYPE MINUS SIGN
2158      011430 104400 012025      TYPE      MINUS          ;TYPE MINUS SIGN
2159      011434 005402      NEG      R2          ;>999. REPLACE IT WITH 999.
2160      011436 020227 001747      POS:    CMP      R2,#999.
2161      011442 003402      BLE      OKAYD
2162      011444 012702 001747      MOV      #999.,R2
2163      011450 105037 014556      OKAYD: CLR8     ONES     ;CLEAR ONES
2164      011454 105037 014555      CLR8     TENS      ;CLEAR TENS
2165      011460 105037 014553      CLR8     HUNS      ;CLEAR HUNS
2166      011464 005702      TESTR2: TST      R2          ;CONVERT VALUE TO A DECIMAL VALUE
2167      011466 001424      BEQ      TYP0UT
2168      011470 005302      DEC      R2
2169      011472 105237 014556      INCB     ONES
2170      011476 123727 014556 000012      CMPB     ONES,#10.
2171      011504 001367      BNE      TESTR2
2172      011506 105037 014556      CLR8     ONES
2173      011512 105237 014555      INCB     TENS
2174      011516 123727 014555 000012      CMPB     TENS,#10.
2175      011524 001357      BNE      TESTR2
2176      011526 105037 014555      CLR8     TENS
2177      011532 105237 014553      INCB     HUNS
2178      011536 000752      BR       TESTR2
2179      011540 152737 000060 014552      TYP0UT: BISB     #60,HUNS    ;PREPARE FOR TYP0UT
2180      011546 152737 000060 014555      BISB     #60,TENS
2181      011554 152737 000060 014556      BISB     #60,ONES
2182      011562 104400 014553      TYPE     ,HUNS          ;TYPE VALUE
2183      011566 000002      RTI
2184      011570 000000      V0:      0          ;TOLERANCE VALUES FOR FUNCTIONAL TESTS
2185      011572 000002      V2:      2
2186      011574 000004      V4:      4
2187      011576 000012      V12:     12
2188      011600 000062      V500:    50.
2189      011602 000326      V326:    326
2190
2191      011604 000041      VNR:      33.        ;33 LSB,NORMAL LIMITS FOR SYSTEM
2192      011606 000310      VNP:      200.       ;2 LSB, INTEGRATION AND FIELD USE ON SPEC TESTS
2193      011610 000144      VSET:     100.
2194      011612 000175      VLIN:     125.
2195      011614 100000      BIT15     ;1 LSB
2196
2197      011616 052777 000100 167320      AGATST:  BIS      #100,#STKS
2198      011624 000137      JMP      #(PC)+
2199      011626 001644      AGTST:    BEGIN

```

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2200 .SBTTL END OF PASS ROUTINE
2201
2202 *****
2203 ;#INCREMENT THE PASS NUMBER ($PASS)
2204 ;#IF THERES A MONITOR GO TO IT
2205 ;#IF THERE ISN'T JUMP TO AGATST
2206
2207 SEOP:
2208 011630 000040 NOP
2209 011632 005037 001102 CLR $STNM ;; ZERO THE TEST NUMBER
2210 011636 005037 001160 CLR $TIMES ;; ZERO THE NUMBER OF ITERATIONS
2211 011642 005237 001202 INC $PASS ;; INCREMENT THE PASS NUMBER
2212 011646 042737 100000 001202 BIC #100000,$PASS ;; DON'T ALLOW A NEG. NUMBER
2213 011654 005327 DEC (PC)+ ;; LOOP?
2214 SEOPCT: .WORD 1
2215 011660 003035 BGT $DOAGN ;; YES
2216 011662 012737 MOV (PC)+,2(PC)+ ;; RESTORE COUNTER
2217 SENDCT: .WORD 1
2218 011666 011656 SEOPCT
2219 011670 104400 011676 TYPE ,65$ ;; TYPE ASCIZ STRING
2220 011674 000414 BR ,64$ ;; GET OVER THE ASCIZ
2221 ;;65$: .ASCIZ (15)<(12)/ENDPASS 'GOOD UNITS /
2222 ;;64$:
2223 011726 013746 001436 MOV GUNITS,-(SP) ;; SAVE GUNITS FOR TYPEOUT
2224 011732 104404 TYPBN ;; GO TYPE--BINARY ASCII
2225 011734 013700 000042 SGET42: MOV @#42,R0 ;; GET MONITOR ADDRESS
2226 011740 001405 BEQ $DOAGN ;; BRANCH IF NO MONITOR
2227 011742 000005 RESET ;; CLEAR THE WORLD
2228 011744 004710 SENMAD: JSR PC,(R0) ;; GO TO MONITOR
2229 011746 000240 NOP ;; SAVE ROOM
2230 011750 000240 NOP ;; FOR
2231 011752 000240 NOP ;; ACT11
2232 011754 $DOAGN:
2233 011754 000137 JMP @ (PC)+ ;; RETURN
2234 011756 011616 SRTMAD: .WORD AGATST
2235 011760 377 377 000 SENULL: .BYTE -1,-1,0 ;; NULL CHARACTER STRING
2236 011764 .EVEN

```

2237					.SBTTL	ASCII MESSAGES
2238	011764	005015	047516	051511	NOIMSG:	.ASCIZ <15><12>/NOISE TEST/<15><12>
2239	011772	020105	042524	052123		
2240	012000	005015	000			
2241	012003	015	051412	052105	SETMSG:	.ASCIZ <15><12>/SETTLING TEST/<15><12>
2242	012010	046124	047111	020107		
2243	012016	042524	052123	005015		
2244	012024	000				
2245	012025	055	000		MINUS:	.BYTE 55,0
2246	012027	077	000		QUEST:	.BYTE 77,0
2247	012031	136	101	040	AMSG:	.BYTE 136,101,40,40,0
2248	012034	040	000			
2249	012036	136	103	040	CMSG:	.BYTE 136,103,40,40,0
2250	012041	040	000			
2251	012043	136	107	015	GMSG:	.BYTE 136,107,15,12,123,127,122,105,107,72,0
2252	012046	012	123	127		
2253	012051	122	105	107		
2254	012054	072	000			
2255	012056	046040	041123	005015	LSBMSG:	.ASCIZ / LSB/<15><12>
2256	012054	000				
2257	012065	055	020055	000	DASH:	.ASCIZ /-- /
2258	012071	123	040524	042524	STATE:	.ASCIZ /STATE-- WIDTH/<15><12>
2259	012076	026455	053440	042111		
2260	012104	04124	005015	000		
2261	012111	103	000110		CH:	.ASCIZ /CH/
2262	012114	020040	020040	000	SPACE:	.ASCIZ / /
2263	012121	040	051514	020102	LSB:	.ASCIZ / LSB ON CH/
2264	012126	047117	041440	000110		
2265	012134	051440	052105	046124	SETCH:	.ASCIZ / SETTLING FROM CH/
2266	012142	047111	020107	051106		
2267	012150	046517	041440	000110		
2268	012156	040440	020124	000	ATMSG:	.ASCIZ / AT /
2269	012163	116	044517	042523	NOI:	.ASCIZ /NOISE: /
2270	012170	020072	000			
2271	012173	040	047117	041440	CHAN:	.ASCIZ / ON CHANNEL /
2272	012200	040510	047116	046105		
2273	012206	000040				
2274	012210	020040	020040	047504	DONE:	.ASCIZ / DONE/<15><12>
2275	012216	042516	005015	000		
2276	012223	057	000		SLASH:	.ASCIZ #/
2277	012228	040	020040	047440	OKMSG:	.ASCIZ / OK/<15><12>
2278	012230	006513	000012			
2279	012236	005015	054524	042520	CCHAN:	.ASCIZ <15><12>/TYPE CHANNEL & CR: /
2280	012244	041440	040510	047116		
2281	012250	046105	023040	041440		
2282	012260	035122	000040			
2283	012264	005015	054524	042520	SEL:	.ASCIZ <15><12>/TYPE "0" FOR OFFSET, "G" FOR GAIN & CR: /
2284	012272	021040	021117	043040		
2285	012300	051117	047440	043106		
2286	012306	042523	026124	021040		
2287	012314	021107	043040	051117		
2288	012322	043440	044501	020116		
2289	012330	020046	051103	020072		
2290	012336	000				

2291	012337	015	040412	045104	XADJ: .ASCII <15><12>/ADJUST R15/
2292	012337	051525	020124	030522	
2293	012337	065			
2294	012337	040	047506	020122	MOLSB: .ASCIZ / FOR 0.00 LSB ERROR/
2295	012337	040	030060	046040	
2296	012337	040	042440	051122	
2297	012337	040	000		
2298	012337	051117	044412	050116	IGND: .ASCII <15><12>/INPUT A GROUND ON THE CHANNEL/
2299	012337	051117	047440	055440	
2300	012337	051117	047125	020104	
2301	012337	051117	052040	042510	
2302	012337	051117	040510	047116	
2303	012337	051117			
2304	012337	051117			
2305	012337	051117			
2306	012337	051117			
2307	012337	051117			
2308	012337	051117			
2309	012337	051117			
2310	012337	051117			
2311	012337	051117			
2312	012337	051117			
2313	012337	051117			
2314	012337	051117			
2315	012337	051117			
2316	012337	051117	040412	045104	YADJ: .ASCIZ <15><12>/ADJUST R3/
2317	012337	051525	020124	031522	
2318	012337	060	000		
2319	012337	040	025052	051105	POSITV: .ASCIZ /+/ ERMSG: .ASCIZ / **ERROR**/<15><12>
2320	012337	040	025122	006452	
2321	012337	040			
2322	012337	040	044513	050120	SKPMMSG: .ASCIZ / SKIPPED STATE(S)/
2323	012337	040	051440	040524	
2324	012337	040	051450	000051	
2325	012337	040	051101	047522	NARMSG: .ASCIZ # NARROW (< 1/2 LSB) STATE(S)#<15><12>
2326	012337	040	026050	030440	
2327	012337	040	046040	041123	
2328	012337	040	052123	052101	
2329	012337	040	045223	005015	
2330	012337	040			
2331	012337	040			
2332	012337	040			
2333	012337	040			
2334	012337	040			
2335	012337	040			
2336	012337	040			
2337	012337	040			
2338	012337	040			
2339	012337	040			
2340	012337	040			
2341	012337	040			
2342	012337	040			
2343	012337	040			
2344	012337	040			
2345	012337	040			
2346	012337	040			
2347	012337	040			
2348	012337	040			
2349	012337	040			
2350	012337	040			
2351	012337	040			
2352	012337	040			
2353	012337	040			
2354	012337	040			
2355	012337	040			
2356	012337	040			
2357	012337	040			
2358	012337	040			
2359	012337	040			
2360	012337	040			
2361	012337	040			
2362	012337	040			
2363	012337	040			
2364	012337	040			
2365	012337	040			
2366	012337	040			
2367	012337	040			
2368	012337	040			
2369	012337	040			
2370	012337	040			
2371	012337	040			
2372	012337	040			
2373	012337	040			
2374	012337	040			
2375	012337	040			
2376	012337	040			
2377	012337	040			
2378	012337	040			
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2397	012337	040			
2398	012337	040			
2399	012337	040			
2400	012337	040			
2401	012337	040			
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2403	012337	040			
2404	012337	040			
2405	012337	040			
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2497	012337	040			
2498	012337	040			
2499	012337	040			
2500	012337	040			

WIDMSG: .ASCIZ # WIDE (> 1 1/2 LSB) STATE(S)#<15><12>

OUTMSG: .ASCIZ / STATE(S) WIDER THAN 2 LSB/

2377	013214	020040	020040	020040
2378	013222	020040	020040	020040
2379	013230	020040	020040	020040
2380	013236	020040	052123	052101
2381	013244	026505	044527	052104
2382	013252	020110	044504	052123
2383	013260	044522	052502	044524
2384	013266	047117	005015	005012
2385	013274	020040	020043	043117
2386	013302	051440	040524	042524
2387	013310	005123	005012	005012
2388	013316	005012	005012	005012
2389	013324	005012	005012	005012
2390	013332	005012	005012	005012
2391	013334	020040	020040	020040
2392	013342	020040	020040	020040
2393	013350	020040	020040	020040
2394	013356	020040	020040	020040
2395	013364	020040	020040	020040
2396	013372	020040	020040	020040
2397	013400	020040	020040	020040
2398	013406	020040	020040	020040
2399	013414	051440	040524	042524
2400	013422	053440	042111	044124
2401	013430	024040	051514	024502
2402	013436	005015		
2403	013440	030040	020040	020040
2404	013446	020040	020040	020040
2405	013454	020040	020040	027461
2406	013462	020062	020040	020040
2407	013470	020040	020040	020040
2408	013476	020040	020061	020040
2409	013504	020040	020040	020040
2410	013512	020040	030440	030440
2411	013520	031057	020040	020040
2412	013526	020040	020040	020040
2413	013534	020040	031040	000
2414	013541	015	052012	050131
2415	013546	020105	042514	052124
2416	013554	051105	023040	041440
2417	013562	020122	047506	020122
2418	013570	042524	052123	020072
2419	013576	000		
2420	013577	122	046105	052101
2421	013604	053111	020105	041501
2422	013612	052503	040522	054503
2423	013620	006472	000012	
2424	013624	046040	041123	046440
2425	013632	054101	046511	046525
2426	013640	040440	020124	000
2427	013645	015	050012	044522
2428	013652	052116	03040	046101
2429	013660	042525	026523	055

MSG16: .ASCII / STATE-WIDTH DISTRIBUTION/<15><12><12><12>

.ASCII / # OF STATES/<12><12><12><12><12><12><12><12><12><12><12><12><12><12><

.ASCII / STATE WIDTH (LSB)/<15>

.ASCIZ # 0 1/2 1 1 1/2 2#

MSG71: .ASCIZ <15><12>/TYPE LETTER & CR FOR TEST: /

MSG21: .ASCIZ /RELATIVE ACCURACY:/<15><12>

LINEA: .ASCIZ / LSB MAXIMUM AT /

HEADS: .ASCII <15><12>/PRINT VALUES--/

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DVADA.CMB

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ASCII MESSAGES

013665	040	042523	020124	ASKCH:	.ASCIZ	/ SET CHANNEL IN SWR LOW BYTE/<15><12>	
013672	044	047101	042516				
013700	020114	047111	051440				
013706	051127	046040	053517				
013714	041040	052131	006505				
013722	000012						
013724	055033	000		C0:	.ASCIZ	<33><132>	
013727	033	015462	000110	C2:	.ASCIZ	<33><62><33><110>	;CLEAR GRAPH MODE AND HOME
013734	000112			C3:	.ASCIZ	<112>	
013736	005015	043117	051506	MOFSET:	.ASCIZ	<15><12>/OFFSET =/	
013744	052105	036440	000				
013751	040	051514	020102	MLSB:	.ASCIZ	/ LSB /	
013756	000						
013757	040	052101	000040	MAT:	.ASCIZ	/ AT /	
013764	005015	042440	052116	METST:	.ASCIZ	<15><12>/ ENTERING TEST /	
013772	051105	047111	020107				
014000	042524	052123	000040				
014006	033	061	101	BUFF1:	.BYTE	33,61,101,61,111,62,114,41,60,45,63,51,66,55,71,61,74,110,41,40,112,0	
014011	061	111	062				
014014	114	041	060				
014017	045	063	051				
014022	066	055	071				
014025	061	074	110				
014030	041	040	112				
014033	000						
014034	033	061	101	BUFF2:	.BYTE	33,61,101,47,111,61,104,50,65,44,62,110,40,40,102,0	
014037	047	111	061				
014042	044	050	065				
014045	044	062	110				
014050	040	040	102				
014053	000						
014054	033	110	033	VTINIT:	.BYTE	33,110,33,112,33,61,101,40,33,62,0	;HOME & ERASE SCREEN & CLEAR GRA
014057	112	033	061				
014062	101	040	033				
014065	062	000					

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DVADA.CMB

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ASCII MESSAGES

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2465	014067	015	005012	042115
2466	014074	030455	026461	053104
2467	014102	042101	026501	020101
2468	014110	020040	040440	053104
2469	014116	030461	042040	040511
2470	014124	047107	051517	044524
2471	014132	006503	012	
2472	014135	012	035101	040440
2473	014142	052125	020117	042524
2474	014150	052123		
2475	014152	005015	035103	041440
2476	014160	046101	041111	040522
2477	014166	044524	047117	
2478	014172	005015	035120	050040
2479	014200	044522	052116	053040
2480	014206	046101	042525	123
2481	014213	015	046012	020072
2482	014220	047514	044507	020103
2483	014226	042524	052123	
2484	014232	005015	035127	053440
2485	014240	040522	040520	047522
2486	014246	047125	020104	042524
2487	014254	052123	005015	000
2488	014261	123	040524	052524
2489	014266	020123	042522	027107
2490	014274	042440	051122	051117
2491	014302	000		
2492	014303	000	044501	042514
2493	014310	020104	047524	044440
2494	014316	052116	051105	052522
2495	014324	052120	000	
2496	014327	123	042516	050130
2497	014334	041505	042524	020104
2498	014342	047111	042524	051122
2499	014350	050123	000124	
2500	014354	051105	047522	020122
2501	014362	047117	040440	042057
2502	014370	041440	040510	047116
2503	014376	046105	000	

HEAD1: .ASCII <15><12><12>/MD-11-DVADA-A ADV11 DIAGNOSTIC/<15><12>

.ASCII <12>/A: AUTO TEST/

.ASCII <15><12>/C: CALIBRATION/

.ASCII <15><12>/P: PRINT VALUES/

.ASCII <15><12>/L: LOGIC TEST/

.ASCIZ <15><12>/W: WRAPAROUND TEST/<15><12>

EM1: .ASCIZ /STATUS REG. ERROR/

EM2: .ASCIZ /FAILED TO INTERRUPT/

EM3: .ASCIZ /UNEXPECTED INTERRUPT/

EM4: .ASCIZ #ERROR ON A/D CHANNEL#

MAINDEC-11-DVADA-A
DVADAA.CMB

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ASCII MESSAGES

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2504	014401	051105	051122	041520
2505	014406	051440	051124	043505
2506	014414	042440	050136	041505
2507	014422	042524	020104	041501
2508	014430	052524	046101	000
2509	014435	105	051122	041520
2510	014442	020040	052123	042522
2511	014447	020107	020040	044103
2512	014456	047101	042516	020114
2513	014464	047040	046517	047111
2514	014472	046101	020040	047524
2515	014500	042514	040522	041516
2516	014506	020105	040440	052103
2517	014514	040525	000114	
2518	014520	051105	050122	020103
2519	014526	020040	020040	051440
2520	014534	051124	043505	020040
2521	014542	020040	041501	052524
2522	014550	046101	000	
2523	014553	000		
2524	014554	056		
2525	014555	000		
2526	014556	000	000	
2529	014560	001116	001316	001124
2530	014566	001126	000000	
2531	014572	001116	001316	001372
2532	014600	001124	001412	001126
2533	014606	000000		
2534	014610	001116	001316	001126
2535	014616	000000		
2536	014620	000000		

DM1: .ASCIZ /ERRPC STREG EXPECTED ACTUAL/

DM2: .ASCIZ /ERRPC STREG CHANNEL NOMINAL TOLERANCE ACTUAL/

DM3: .ASCIZ /ERRPC STREG ACTUAL/

MUNS: .BYTE 0
 DECPNT: .BYTE 56
 TENS: .BYTE 0
 ONES: .BYTE 0,0
 .EVEN

DT1: SERRPC, STREG, \$GDDAT, \$BDDAT, 0

DT2: SERRPC, STREG, CHANL, \$GDDAT, SPREAD, \$BDDAT, 0

DT3: SERRPC, STREG, \$BDDAT, 0

DF1: 0

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.SBTTL TTY INPUT ROUTINE

::*****

.ENABL LSB

.DSABL LSB

::*****

::THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY

::CALL:

:: ROCHR
:: RETURN HERE

:: INPUT A SINGLE CHARACTER FROM THE TTY
:: CHARACTER IS ON THE STACK
:: WITH PARITY BIT STRIPPED OFF

SRROCHR: MOV (SP), -(SP)
MOV 4(SP), 2(SP)
1S: TSTB 2STKS
BPL 1S
MOVB 2STKB, 4(SP)
BIC 8(C177), 4(SP)
CMP 4(SP), 823
BNE 3S
2S: TSTB 2STKS
BPL 2S
MOVB 2STKB, -(SP)
BIC 8(C177), (SP)
CMP (SP)+, 821
BNE 2S
BR 1S
3S: CMP 4(SP), 8140
BLT 4S
CMP 4(SP), 8175
BGT 4S
BIC 840, 4(SP)
4S: RTI

:: PUSH DOWN THE PC
:: SAVE THE PS
:: WAIT FOR
:: A CHARACTER
:: READ THE TTY
:: GET RID OF JUNK IF ANY
:: IS IT A CONTROL-S?
:: BRANCH IF NO
:: WAIT FOR A CHARACTER
:: LOOP UNTIL ITS THERE
:: GET CHARACTER
:: MAKE IT 7-BIT ASCII
:: IS IT A CONTROL-Q?
:: IF NOT DISCARD IT
:: YES, RESUME
:: IS IT UPPER CASE?
:: BRANCH IF YES
:: IS IT A SPECIAL CHAR?
:: BRANCH IF YES
:: MAKE IT UPPER CASE
:: GO BACK TO USER

::*****

::THIS ROUTINE WILL INPUT A STRING FROM THE TTY

::CALL:

:: RDLIN
:: RETURN HERE

:: INPUT A STRING FROM THE TTY
:: ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
:: TERMINATOR WILL BE A BYTE OF ALL 0'S

SRDLIN: MOV R3, -(SP)
1S: MOV 8TTYIN, R3
2S: CMP 8TTYIN+8, R3
BLOS 4S
ROCHR
MOVB (SP)+, (R3)
10S: CMPB 8177, (R3)
BNE 3S
4S: TYPE 8QUES
BR 1S
3S: MOVB (R3), 9S
TYPE , 9S

:: SAVE R3
:: GET ADDRESS
:: BUFFER FULL?
:: BR IF YES
:: GO READ ONE CHARACTER FROM THE TTY
:: GET CHARACTER
:: IS IT A RUBOUT
:: SKIP IF NOT
:: TYPE A ''
:: CLEAR THE BUFFER AND LOOP
:: ECHO THE CHARACTER

014622 011646
014624 016666 000004 000002
014632 105777 164306
014636 100375
014640 117766 164302 000004
014646 042766 177600 000004
014654 026627 000004 000023
014662 001013
014664 105777 164254
014670 100375
014672 117746 164250
014676 042716 177600
014702 022627 000021
014706 001366
014710 000750
014712 026627 000004 000140
014720 002407
014722 026627 000004 000175
014730 003003
014732 042766 000040 000004
014740 000002
014742 010346
014744 012703 015050
014750 022703 015060
014754 101405
014756 104405
014760 112613
014762 122713 000177
014766 001003
014770 104400 001170
014774 000763
014776 111337 015046
015002 104400 015046

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 DVADAA.CMB READ AN OCTAL NUMBER FROM THE TTY

.SBTTL READ AN OCTAL NUMBER FROM THE TTY

```

*****
*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
*CHANGE IT TO BINARY.
*CALL:
*      RDOCT          ;; READ AN OCTAL NUMBER
*      RETURN HERE   ;; LOW ORDER BITS ARE ON TOP OF THE STACK
*                   ;; HIGH ORDER BITS ARE IN $HIOCT
  
```

2611
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2640
2641
2642
2643
2644
2645
2646
2647
2648

```

015114 011646
015116 016666 000004 000002
015124 010046
015126 010146
015130 010246
015132 104406
015134 012500
015136 005001
015140 005002
015142 112046
015144 001412
015146 006301
015150 006102
015152 006301
015154 006102
015156 006301
015160 006102
015162 042716 177770
015166 062601
015170 000764
015172 005726
015174 010166 000012
015200 010237 015214
015204 012602
015206 012601
015210 012600
015212 000002
015214 000000
  
```

```

SRDOCT: MOV      (SP), -(SP)          ;; PROVIDE SPACE FOR THE
        MOV      4(SP), 2(SP)        ;; INPUT NUMBER
        MOV      RO, -(SP)           ;; PUSH RO ON STACK
        MOV      R1, -(SP)           ;; PUSH R1 ON STACK
        MOV      R2, -(SP)           ;; PUSH R2 ON STACK
1$:     RDLIN                      ;; READ AN ASCII LINE
        MOV      (SP)+, RO           ;; GET ADDRESS OF 1ST CHARACTER
        CLR      R1                  ;; CLEAR DATA WORD
        CLR      R2
2$:     MOVVB    (RO)+, -(SP)         ;; PICKUP THIS CHARACTER
        BEQ      3$                  ;; IF ZERO GET OUT
        ASL      R1                  ;; *2
        ROL      R2
        ASL      R1                  ;; *4
        ROL      R2
        ASL      R1                  ;; *8
        ROL      R2
        BIC      #1C7, (SP)          ;; STRIP THE ASCII JUNK
        ADD      (SP)+, R1           ;; ADD IN THIS DIGIT
        BR       2$                  ;; LOOP
3$:     TST      (SP)+                ;; CLEAN TERMINATOR FROM STACK
        MOV      R1, 12(SP)          ;; SAVE THE RESULT
        MOV      R2, $HIOCT
        MOV      (SP)+, R2           ;; POP STACK INTO R2
        MOV      (SP)+, R1           ;; POP STACK INTO R1
        MOV      (SP)+, RO           ;; POP STACK INTO RO
        RTI                          ;; RETURN
$HIOCT: .WORD    0                   ;; HIGH ORDER BITS GO HERE
  
```

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MAINDEC-11-DVADA-A
DVADAA.CMB

MACY11 27(732)
SCOPE HANDLER ROUTINE

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.SBTTL SCOPE HANDLER ROUTINE

```

*****
: THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
: AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
: AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
: THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
: SW14=1      LOOP ON TEST
: SW11=1      INHIBIT ITERATIONS
: SW09=1      LOOP ON ERROR
: SW08=1      LOOP ON TEST IN SWR<7:0>
: CALL
: SCOPE          ;;SCOPE=IOT

```

```

$SCOPE:
1$: BIT      #BIT14, $SWR      ;; LOOP ON PRESENT TEST?
   BNE      $OVER            ;; YES IF SW14=1
: START OF CODE FOR THE XOR TESTER#####
$XTSTR: BR      6$
   MOV      @#ERRVEC, -(SP)   ;; IF RUNNING ON THE "XOR" TESTER CHANGE
   MOV      #5$, @#ERRVEC    ;; THIS INSTRUCTION TO A "NOP" (NOP=240)
   TST     @#177060          ;; SAVE THE CONTENTS OF THE ERROR VECTOR
   MOV      (SP)+, @#ERRVEC   ;; SET FOR TIMEOUT
   BR      $SVLAD            ;; TIME OUT ON XOR?
   CMP     (SP)+, (SP)+      ;; RESTORE THE ERROR VECTOR
   MOV      (SP)+, @#ERRVEC   ;; GO TO THE NEXT TEST
   BR      7$                ;; CLEAR THE STACK AFTER A TIME OUT
5$:      MOV      (SP)+, @#ERRVEC ;; RESTORE THE ERROR VECTOR
   BR      7$                ;; LOOP ON THE PRESENT TEST
6$: ; ##### END OF CODE FOR THE XOR TESTER#####
   BIT     #BIT08, $SWR      ;; LOOP ON SPEC. TEST?
   BEQ     2$                ;; BR IF NO
   CMPB   @SWR, $STNM        ;; ON THE RIGHT TEST? SWR<7:0>
   BEQ     $OVER            ;; BR IF YES
2$:      TSTB   $ERFLG        ;; HAS AN ERROR OCCURRED?
   BEQ     3$                ;; BR IF NO
   CMPB   $ERMAX, $ERFLG    ;; MAX. ERRORS FOR THIS TEST OCCURRED?
   BHI     3$                ;; BR IF NO
   BIT     #BIT09, $SWR      ;; LOOP ON ERROR?
   BEQ     4$                ;; BR IF NO
7$:      MOV     $LPERR, $LPADR ;; SET LOOP ADDRESS TO LAST SCOPE
   BR     $OVER
4$:      CLRB   $ERFLG        ;; ZERO THE ERROR FLAG
   CLR    $TIMES            ;; CLEAR THE NUMBER OF ITERATIONS TO MAKE
   BR     1$                ;; ESCAPE TO THE NEXT TEST
3$:      BIT     #BIT11, $SWR  ;; INHIBIT ITERATIONS?
   BNE     1$                ;; BR IF YES
   TST    $PASS            ;; IF FIRST PASS OF PROGRAM
   BEQ    1$                ;; INHIBIT ITERATIONS
   INC    $ICNT            ;; INCREMENT ITERATION COUNT
   CMP    $TIMES, $ICNT     ;; CHECK THE NUMBER OF ITERATIONS MADE
   BGE    $OVER            ;; BR IF MORE ITERATION REQUIRED
   MOV    #1, $ICNT        ;; REINITIALIZE THE ITERATION COUNTER
   MOV    $MXCNT, $TIMES   ;; SET NUMBER OF ITERATIONS TO DO
$SVLAD: INCB   $STNM        ;; COUNT TEST NUMBERS
   MOVB  $STNM, $TESTN     ;; SET TEST NUMBER IN APT MAILBOX
   MOV   (SP), $LPADR     ;; SAVE SCOPE LOOP ADDRESS

```

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2664	015216	032777	040000	163714	
2665	015224	001114			
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2667	015226	000416			
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2669	015230	013746	000004		
2670	015234	012737	015254	000004	
2671	015242	005737	177060		
2672	015246	012637	000004		
2673	015252	000463			
2674	015254	022626			
2675	015256	012637	000004		
2676	015262	000423			
2677	015264				
2678	015264	032777	000400	163646	
2679	015272	001404			
2680	015274	127737	163640	001102	
2681	015302	001465			
2682	015304	105737	001103		
2683	015310	001421			
2684	015312	123737	001115	001103	
2685	015320	101015			
2686	015322	032777	001000	163610	
2687	015330	001404			
2688	015332	013737	001110	001106	
2689	015340	000446			
2690	015342	105037	001103		
2691	015346	005237	001160		
2692	015352	000415			
2693	015354	032777	004000	163556	
2694	015362	001011			
2695	015364	005737	001202		
2696	015370	001406			
2697	015372	005237	001104		
2698	015376	023737	001160	001104	
2699	015404	002024			
2700	015406	012737	000001	001104	
2701	015414	013737	015472	001160	
2702	015422	105237	001102		
2703	015426	113737	001102	001200	
2704	015434	011637	001106		

```

2705 015440 011637 001110      MOV      (SP), $LPERR      ;; SAVE ERROR LOOP ADDRESS
2706 015444 005037 001162      CLR      $ESCAPE          ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
2707 015450 112737 000001 001115  MOVB     #1, $ERRMAX       ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
2708 015456 013777 001102 163456 $OVER:  MOV     $STNM, $DISPLAY  ;; DISPLAY TEST NUMBER
2709 015464 013716 001106      MOV     $LPADR, (SP)      ;; FUDGE RETURN ADDRESS
2710 015470 000002      RTI                          ;; FIXES PS
2711 015472 003720 $MXCNT: 2000.              ;; MAX. NUMBER OF ITERATIONS
2712      .SBTTL  ERROR HANDLER ROUTINE
2713
2714      ;*****
2715      ;*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
2716      ;*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
2717      ;*AND GO TO $ERRTYP ON ERROR
2718      ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
2719      ;*$SW15=1      HALT ON ERROR
2720      ;*$SW13=1      INHIBIT ERROR TYPEOUTS
2721      ;*$SW10=1      BELL ON ERROR
2722      ;*$SW09=1      LOOP ON ERROR
2723      ;*$CALL
2724      ;*      ERROR      N      ;; ERROR=EMT AND N=ERROR ITEM NUMBER
2725
2726 015474      $ERROR:
2727 015474 043737 001440 001436 7$:      BIC      TSTBIT, GUNITS
2728 015502 105237 001103      INCB     $ERFLG          ;; SET THE ERROR FLAG
2729 015506 001775      BEQ     7$              ;; DON'T LET THE FLAG GO TO ZERO
2730 015510 013777 001102 163424  MOV     $STNM, $DISPLAY  ;; DISPLAY TEST NUMBER AND ERROR FLAG
2731 015516 032777 002000 163414  BIT     #BIT10, $SWR     ;; BELL ON ERROR?
2732 015524 001402      BEQ     1$              ;; NO - SKIP
2733 015526 104400 001164      TYPE    $BELL          ;; RING BELL
2734 015532 005237 001112 1$:      INC     $ERTTL         ;; COUNT THE NUMBER OF ERRORS
2735 015536 011637 001116      MOV     (SP), $ERRPC    ;; GET ADDRESS OF ERROR INSTRUCTION
2736 015542 162737 000002 001116  SUB     #2, $ERRPC
2737 015550 117737 163342 001114  MOVB    $ERRPC, $ITEMB   ;; STRIP AND SAVE THE ERROR ITEM CODE
2738 015556 032777 020000 163354  BIT     #BIT13, $SWR     ;; SKIP TYPEOUT IF SET
2739 015564 001004      BNE     20$            ;; SKIP TYPEOUTS
2740 015566 004737 015676      JSR     PC, $ERRTYP     ;; GO TO USER ERROR ROUTINE
2741 015572 104400 001171      TYPE    $CRLF
2742
2743 015576 122737 000001 001214 20$:     CMPB    #APTENV, $ENV    ;; RUNNING IN APT MODE
2744 015604 001007      BNE     2$              ;; NO SKIP APT ERROR REPORT
2745 015606 113737 001114 015620  MOVB    $ITEMB, 21$     ;; SET ITEM NUMBER AS ERROR NUMBER
2746 015614 004737 016332      JSR     PC, $ATY4      ;; REPORT FATAL ERROR TO APT
2747 015620 000      .BYTE  0
2748 015621 000      .BYTE  0
2749 015622 000777 22$:     BR      22$            ;; APT ERROR LOOP
2750 015624 005777 163310 2$:      TST     $SWR          ;; HALT ON ERROR
2751 015630 100001 3$:      BFL     3$              ;; SKIP IF CONTINUE
2752 015632 000000      HALT                    ;; HALT ON ERROR!
2753 015634 032777 001000 163276 3$:      BIT     #BIT09, $SWR   ;; LOOP ON ERROR SWITCH SET?
2754 015642 001402      BEQ     4$              ;; BR IF NO
2755 015644 013716 001110      MOV     $LPERR, (SP)    ;; FUDGE RETURN FOR LOOPING
2756 015650 005737 001162 4$:      TST     $ESCAPE        ;; CHECK FOR AN ESCAPE ADDRESS
2757 015654 001402      BEQ     5$              ;; BR IF NONE
2758 015656 013716 001162      MOV     $ESCAPE, (SP)  ;; FUDGE RETURN ADDRESS FOR ESCAPE
2759 015662 5$:
2760 015662 022737 011744 000042 5$:      CMP     #SENDAC, $42   ;; ACT-11 AUTO-ACCEPT?

```

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2761 015670 001001      BNE      6S      ;; BRANCH IF NO
2762 015672 000000      HALT          ;; YES
2763 015674 000002      6S:
2764 015674 000002      RTI          ;; RETURN
2765 .SBTTL  ERROR MESSAGE TIMEOUT ROUTINE
2766
2767 *****
2768 *THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
2769 *ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
2770 *AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.
2771
2772 015676 SERRTYP:
2773 015676 104400 001171      TYPE      $CRLF      ;; "CARRIAGE RETURN" & "LINE FEED"
2774 015702 010046      MOV      RO,-(SP)    ;; SAVE RO
2775 015704 005000      CLR      RO          ;; PICKUP THE ITEM INDEX
2776 015706 153700 001114      BISB     @($ITEMB,RO
2777 015712 001004      BNE      1S          ;; IF ITEM NUMBER IS ZERO, JUST
2778                                ;; TYPE THE PC OF THE ERROR
2779 015714 013746 001116      MOV      $ERRPC,-(SP) ;; SAVE $ERRPC FOR TYPEOUT
2780                                ;; ERROR ADDRESS
2781 015720 104401      TYPOC     ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
2782 015722 000426      BR      6S          ;; GET OUT
2783 015724 005300      1S:      DEC      RO          ;; ADJUST THE INDEX SO THAT IT WILL
2784 015726 006300      ASL     RO          ;; WORK FOR THE ERROR TABLE
2785 015730 006300      ASL     RO
2786 015732 006300      ASL     RO
2787 015734 062700 001256      ADD      @($ERRTB,RO ;; FORM TABLE POINTER
2788 015740 012037 015750      MOV      (RO)+,2S   ;; PICKUP "ERROR MESSAGE" POINTER
2789 015744 001404      BEQ     3S          ;; SKIP TYPEOUT IF NO POINTER
2790 015746 104400      TYPE     ;; TYPE THE "ERROR MESSAGE"
2791 015750 000000      2S:      .WORD    0        ;; "ERROR MESSAGE" POINTER GOES HERE
2792 015752 104400 001171      TYPE     $CRLF      ;; "CARRIAGE RETURN" & "LINE FEED"
2793 015756 012037 015766      3S:      MOV      (RO)+,4S   ;; PICKUP "DATA HEADER" POINTER
2794 015762 001404      BEQ     5S          ;; SKIP TYPEOUT IF 0
2795 015764 104400      TYPE     ;; TYPE THE "DATA HEADER"
2796 015766 000000      4S:      .WORD    0        ;; "DATA HEADER" POINTER GOES HERE
2797 015770 104400 001171      TYPE     $CRLF      ;; "CARRIAGE RETURN" & "LINE FEED"
2798 015774 011000      5S:      MOV      (RO),RO    ;; PICKUP "DATA TABLE" POINTER
2799 015776 001004      BNE     7S          ;; GO TYPE THE DATA
2800 016000 012600      6S:      MOV      (SP)+,RO    ;; RESTORE RO
2801 016002 104400 001171      TYPE     $CRLF      ;; "CARRIAGE RETURN" & "LINE FEED"
2802 016006 000207      RTS      PC          ;; RETURN
2803 016010      7S:
2804 016010 013046      MOV      @2(RO)+,-(SP) ;; SAVE @2(RO)+ FOR TYPEOUT
2805 016012 104401      TYPOC     ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
2806 016014 005710      TST     (RO)        ;; IS THERE ANOTHER NUMBER?
2807 016016 001770      BEQ     6S          ;; BR IF NO
2808 016020 104400 016026      TYPE     2S        ;; TYPE TWO(2) SPACES
2809 016024 000771      BR      7S          ;; LOOP
2810 016026 020040 000      8S:      .ASCIZ  / /
2811 016032 .EVEN

```

.SBTTL TYPE ROUTINE

*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1: \$NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2: \$FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3: \$FILLC CONTAINS THE CHARACTER TO FILL AFTER.

*CALL:
*1) USING A TRAP INSTRUCTION
* TYPE ,MESADR ;; MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
* TYPE
* MESADR
*

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STYPE: TSTB STPFLG
BPL 18
HALT
BR 38
18: MOV R0, -(SP)
MOV 22(SP), R0
CMPB #APTENV, SENV
BNE 628
BITB #APTSPOOL, SENVM
BEQ 628
MOV R0, 618
JSR PC, \$ATY3
618: .WORD 0
628: BITB #APTCSUP, SENVM
BNE 608
28: MOVB (R0)+, -(SP)
BNE 48
TST (SP)+
608: MOV (SP)+, R0
38: ADD #2, (SP)
RTI
48: CMPB #HT, (SP)
BEQ 88
CMPB #CRLF, (SP)
BNE 58
TST (SP)+
TYPE
\$CRLF
CLRB \$CHARCNT
BR 28
58: JSR PC, STYPEC
68: CMPB \$FILLC, (SP)+
BNE 28
MOV \$NULL, -(SP)
78: DECB 1(SP)
BLT 68
JSR PC, STYPEC
DECB \$CHARCNT

;; IS THERE A TERMINAL?
BR IF YES
HALT HERE IF NO TERMINAL
LEAVE
SAVE R0
GET ADDRESS OF ASCIZ STRING
RUNNING IN APT MODE
NO GO CHECK FOR APT CONSOLE
SPOOL MESSAGE TO APT
NO GO CHECK FOR CONSOLE
SETUP MESSAGE ADDRESS FOR APT
SPOOL MESSAGE TO APT
MESSAGE ADDRESS
APT CONSOLE SUPPRESSED
YES, SKIP TYPE OUT
PUSH CHARACTER TO BE TYPED ONTO STACK
BR IF IT ISN'T THE TERMINATOR
IF TERMINATOR POP IT OFF THE STACK
RESTORE R0
ADJUST RETURN PC
RETURN
BRANCH IF <HT>
;; BRANCH IF NOT <CRLF>
;; POP <CR><LF> EQUIV
;; TYPE A CR AND LF
;; CLEAR CHARACTER COUNT
;; GET NEXT CHARACTER
;; GO TYPE THIS CHARACTER
;; IS IT TIME FOR FILLER CHARS.?
;; IF NO GO GET NEXT CHAR.
;; GET # OF FILLER CHARS. NEEDED
;; AND THE NULL CHAR.
;; DOES A NULL NEED TO BE TYPED?
;; BR IF NO--GO POP THE NULL OFF OF STACK
;; GO TYPE A NULL
;; DO NOT COUNT AS A COUNT


```

2868 016216 000770          BR      75          ;;LOOP
2869
2870          ;HORIZONTAL TAB PROCESSOR
2871
2872 016220 112716 000040      BS:    MOVB    0' (SP)          ;; REPLACE TAB WITH SPACE
2873 016224 004737 016244      SS:    JSR     PC,$TYPEC        ;; TYPE A SPACE
2874 016230 132737 000007 016310      BITB   #7,$CHARCNT          ;; BRANCH IF NOT AT
2875 016236 001372          BNE     95                    ;; TAB STOP
2876 016240 005726          TST    (SP)+                ;; POP SPACE OFF STACK
2877 016242 000724          BR      25                    ;; GET NEXT CHARACTER
2878 016244 105777 162700      $TYPEC: TSTB   $STPB          ;; WAIT UNTIL PRINTER IS READY
2879 016250 100375          BPL    $TYPEC
2880 016252 116677 000002 162672      MOVB   2(SP),2$TPB          ;; LOAD CHAR TO BE TYPED INTO DATA REG.
2881 016260 122766 000015 000002      CMPB   $CR,2(SP)           ;; IS CHARACTER A CARRIAGE RETURN?
2882 016266 001003          BNE     1$                    ;; BRANCH IF NO
2883 016270 105037 016310      CLRB   $CHARCNT           ;; YES--CLEAR CHARACTER COUNT
2884 016274 000406          BR      $TYPEX              ;; EXIT
2885 016276 122766 000012 000002      1$:    CMPB   #LF,2(SP)      ;; IS CHARACTER A LINE FEED?
2886 016304 001402          BEQ    $TYPEX              ;; BRANCH IF YES
2887 016306 105227          INCB   (PC)+                ;; COUNT THE CHARACTER
2888 016310 000000          $CHARCNT: .WORD 0          ;; CHARACTER COUNT STORAGE
2889 016312 000207          $TYPEX:  RTS               PC
2890
2891          .SETTL  APT COMMUNICATIONS ROUTINE
2892
2893          ;*****
2894 016314 112737 000001 016560      $ATY1: MOVB   #1,$FFLG        ;; TO REPORT FATAL ERROR
2895 016322 112737 000001 016556      $ATY3: MOVB   #1,$MFLG        ;; TO TYPE A MESSAGE
2896 016330 000403          BR      $ATYC
2897 016332 112737 000001 016560      $ATY4: MOVB   #1,$FFLG        ;; TO ONLY REPORT FATAL ERROR
2898 016340          $ATYC:
2899 016340 010046          MOV    R0,-(SP)            ;; PUSH R0 ON STACK
2900 016342 010146          MOV    R1,-(SP)            ;; PUSH R1 ON STACK
2901 016344 105737 016556          TSTB   $MFLG              ;; SHOULD TYPE A MESSAGE?
2902 016350 001450          BEQ    5$                  ;; IF NOT: BR
2903 016352 122737 000001 001214      CMPB   $APTENV,$ENV        ;; OPERATING UNDER APT?
2904 016360 001031          BNE     3$                  ;; IF NOT: BR
2905 016362 132737 000100 001215      BITB   $APTPOOL,$ENVM      ;; SHOULD SPOOL MESSAGES?
2906 016370 001425          BEQ    3$                  ;; IF NOT: BR
2907 016372 017600 000004          MOV    #4(SP),R0           ;; GET MESSAGE ADDR.
2908 016376 062766 000002 000004      ADD    #2,4(SP)            ;; BUMP RETURN ADDR.
2909 016404 005737 001174          1$:    TST    $MSGTYPE        ;; SEE IF DONE W/ LAST XMISSION?
2910 016410 001375          BNE     1$                  ;; IF NOT: WAIT
2911 016412 010037 001210      MOV    R0,$MSGAD           ;; PUT ADDR IN MAILBOX
2912 016416 105720          2$:    TSTB   (R0)+          ;; FIND END OF MESSAGE
2913 016420 001376          BNE     2$
2914 016422 163700 001210      SUB    $MSGAD,R0           ;; SUB START OF MESSAGE
2915 016426 006200          ASR    R0                  ;; GET MESSAGE LGTH IN WORDS
2916 016430 010037 001212      MOV    R0,$MSGLG          ;; PUT LENGTH IN MAILBOX
2917 016434 012737 000004 001174      MOV    #4,$MSGTYPE        ;; TELL APT TO TAKE MSG.
2918 016442 000413          BR      5$
2919 016444 017637 000004 016470      3$:    MOV    #4(SP),4$        ;; PUT MSG ADDR IN JSR LINKAGE
2920 016452 062766 000002 000004      ADD    #2,4(SP)            ;; BUMP RETURN ADDRESS
2921 016460 013746 177776          MOV    177776,-(SP)       ;; PUSH 177776 ON STACK
2922 016464 004737 016032          JSR    PC,$TYPE           ;; CALL TYPE MACRO
2923 016470 000000          4$:    .WORD 0

```

```

2944 016472 105737 016560 55:
2945 016472 001416 105: TSTB SFFLG
2946 016476 005737 001214 125: BEQ 125
2947 016500 001413 005737 115: TST SENV
2948 016504 005737 001174 115: BEQ 125
2949 016513 001375 000004 001176 115: TST MSGTYPE
2950 016517 017637 000002 000004 115: BNE 115
2951 016521 005237 001174 125: MOV #4(SP),SFATAL
2952 016525 105037 016560 125: ADD #2,4(SP)
2953 016529 105037 016557 125: INC MSGTYPE
2954 016533 105037 016556 125: CLRB SFFLG
2955 016537 012601 125: CLRB SLFLG
2956 016541 012600 125: CLRB SMFLG
2957 016545 000207 125: MOV (SP)+,R1
2958 016549 000 125: MOV (SP)+,R0
2959 016553 000 125: RTS PC
2960 016557 000 125: D 0
2961 016560 000 125: D 0
2962 016562 000 125: D 0
2963 000200 125: SMFLG: .BYTE
2964 000001 125: SLFLG: .BYTE
2965 000100 125: SFFLG: .BYTE
2966 000040 125: .EVEN
2967 000001 APTSIZE=200
2968 000100 APTENV=001
2969 000040 APTSPool=100
2970 000001 APTCSUP=040

```

```

:: SHOULD REPORT FATAL ERROR?
:: IF NOT: BR
:: RUNNING UNDER APT?
:: IF NOT: BR
:: FINISHED LAST MESSAGE?
:: IF NOT: WAIT
:: GET ERROR #
:: BUMP RETURN ADDR.
:: TELL APT TO TAKE ERROR
:: CLEAR FATAL FLAG
:: CLEAR LOG FLAG
:: CLEAR MESSAGE FLAG
:: POP STACK INTO R1
:: POP STACK INTO R0
:: RETURN
:: MESSG. FLAG
:: LOG FLAG
:: FATAL FLAG

```

.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

```

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOS   ;;CALL FOR TYPEOUT
*   .BYTE  N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*   .BYTE  M              ;;M=1 OR 0
*                               ;;1=TYPE LEADING ZEROS
*                               ;;0=SUPPRESS LEADING ZEROS

```

```

*STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*STYPOS OR STYPOC
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPON   ;;CALL FOR TYPEOUT

```

```

*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOC   ;;CALL FOR TYPEOUT

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2998
2999
3000
3001
3002
3003

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016562 017646 000000
016566 116637 000001 017005
016574 112637 017007
016600 062716 000002
016604 000406
016606 112737 000001 017005
016614 112737 000006 017007
016622 112737 000005 017004
016630 010346
016638 010446
016646 010546
016654 113704 017007
016662 062716 000006
016670 110427 017006
016678 113704 017005
016686 016600 000012
016694 006103
016702 006103
016710 105337 017006
016718 100016
016726 042703 177770
016734 001002
016742 005704
016750 001403

```

```

STYPOS: MOV     2(SP),-(SP)      ;; PICKUP THE MODE
        MOV     1(SP),SOFILL    ;; LOAD ZERO FILL SWITCH
        MOV     (SP)+,SOMODE+1  ;; NUMBER OF DIGITS TO TYPE
        ADD     #2,(SP)         ;; ADJUST RETURN ADDRESS
        BR      STYPON
STYPOC: MOV     #1,SOFILL       ;; SET THE ZERO FILL SWITCH
        MOV     #6,SOMODE+1     ;; SET FOR SIX(6) DIGITS
STYPON: MOV     #5,SOCNT        ;; SET THE ITERATION COUNT
        MOV     R3,-(SP)        ;; SAVE R3
        MOV     R4,-(SP)        ;; SAVE R4
        MOV     R5,-(SP)        ;; SAVE R5
        MOV     SOMODE+1,R4     ;; GET THE NUMBER OF DIGITS TO TYPE
        NEG     R4
        ADD     #6,R4           ;; SUBTRACT IT FOR MAX. ALLOWED
        MOV     R4,SOMODE       ;; SAVE IT FOR USE
        MOV     SOFILL,R4       ;; GET THE ZERO FILL SWITCH
        MOV     12(SP),R5      ;; PICKUP THE INPUT NUMBER
        CLR     R3              ;; CLEAR THE OUTPUT WORD
15:    ROL     R5                ;; ROTATE MSB INTO "C"
        BR     25:              ;; GO DO MSB
25:    ROL     R5                ;; FORM THIS DIGIT
        ROL     R5
        ROL     R5
        MOV     R5,R3
35:    ROL     R3                ;; GET LSB OF THIS DIGIT
        DECB   SOMODE           ;; TYPE THIS DIGIT?
        BPL    7$              ;; BR IF NO
        BIC    #177770,R3      ;; GET RID OF JUNK
        BEQ   4$              ;; TEST FOR 0
        TST   R4               ;; SUPPRESS THIS 0?
        BEQ   5$              ;; BR IF YES

```

3004	016724	005204		4S:	INC	R4	:: DON'T SUPPRESS ANYMORE 0'S
3005	016726	052703	000060		BIS	#'0,R3	:: MAKE THIS DIGIT ASCII
3006	016732	052703	000040	5S:	BIS	#' R3	:: MAKE ASCII IF NOT ALREADY
3007	016736	110337	017002		MOVB	R3,8S	:: SAVE FOR TYPING
3008	016742	104400	017002		TYPE	8S	:: GO TYPE THIS DIGIT
3009	016746	105337	017004	7S:	DECB	\$OCNT	:: COUNT BY 1
3010	016752	003347			BGT	2S	:: BR IF MORE TO DO
3011	016754	002402			BLT	6S	:: BR IF DONE
3012	016756	005204			INC	R4	:: INSURE LAST DIGIT ISN'T A BLANK
3013	016760	000744			BR	2S	:: GO DO THE LAST DIGIT
3014	016762	012605		6S:	MOV	(SP)+,R5	:: RESTORE R5
3015	016764	012604			MOV	(SP)+,R4	:: RESTORE R4
3016	016766	012603			MOV	(SP)+,R3	:: RESTORE R3
3017	016770	016666	000002 000004		PJV	2(SP),4(SP)	:: SET THE STACK FOR RETURNING
3018	016776	012616			MOV	(SP)+,(SP)	
3019	017000	000002			RTI		:: RETURN
3020	017002	000		8S:	.BYTE	0	:: STORAGE FOR ASCII DIGIT
3021	017003	000			.BYTE	00	:: TERMINATOR FOR TYPE ROUTINE
3022	017004	000		\$OCNT:	.BYTE	00	:: OCTAL DIGIT COUNTER
3023	017005	000		\$OFILL:	.BYTE	00	:: ZERO FILL SWITCH
3024	017006	000000		\$OMODE:	.WORD	0	:: NUMBER OF DIGITS TO TYPE

```

3025          .SBTTL  BINARY TO ASCII AND TYPE ROUTINE
3026
3027          ;*****
3028          ;THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 16-BIT
3029          ;BINARY-ASCII NUMBER AND TYPE IT.
3030          ;CALL:
3031          ;*      MOV      NUMBER,-(SP)      ;;NUMBER TO BE TYPED
3032          ;*      TYPBN                      ;;TYPE IT
3033
3034          $TYPBN: MOV      R1,-(SP)          ;;SAVE R1 ON THE STACK
3035          MOV      6(SP),R1                ;;GET THE INPUT NUMBER
3036          SEC                                  ;;SET "C" SO CAN KEEP TRACK OF THE NUMBER OF BITS
3037          MOV     0,'0,$BIN                ;;SET CHARACTER TO AN ASCII "0".
3038          ROL     R1                        ;;GET THIS BIT
3039          BEQ     2$                        ;;DONE?
3040          AOCB   $BIN                      ;;NO--SET THE CHARACTER EQUAL TO THIS BIT
3041          TYPE   ,SBIN                     ;;GO TYPE THIS BIT
3042          CLC                                  ;;CLEAR "C" SO CAN KEEP TRACK OF BITS
3043          BR     1$                          ;;GO DO THE NEXT BIT
3044          MOV     (SP)+,R1                  ;;POP THE STACK INTO R1
3045          MOV     2(SP),4(SP)              ;;ADJUST THE STACK
3046          MOV     (SP)+,(SP)
3047          RTI
3048          $BIN:  .BYTE  0,0                ;;RETL'ON TO USER
                                           ;;STORAGE FOR ASCII CHAR. AND TERMINATOR

```

```

3049
3050
3051
3052
3053
3054
3055
3056
3057 017064 010046
3058 017066 016600 000002
3059 017072 005740
3060 017074 111000
3061 017076 006300
3062 017100 016000 017106
3063 017104 000200
3064
3065
3066
3067
3068
3069
3070
3071
3072 017106
3073 017106 016032
3074 017110 016606
3075 017112 016562
3076 017114 016622
3077 017116 017010
3078
3079
3080 017120 014622
3081 017122 014742
3082 017124 015114
3083 017126 004102
3084 017130 004074
3085 017132 011424

```

.SBTTL TRAP DECODER

```

:*****
:THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
:AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
:OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
:GO TO THAT ROUTINE.

```

```

$TRAP:  MOV    RO -(SP)           ;; SAVE RO
        MOV    2(SP),RO         ;; GET TRAP ADDRESS
        TST    -(RO)           ;; BACKUP BY 2
        MOVB   (RO),RO         ;; GET RIGHT BYTE OF TRAP
        ASL    RO              ;; POSITION FOR INDEXING
        MOV    $TRPAD(RO),RO    ;; INDEX TO TABLE
        RTS    RO              ;; GO TO ROUTINE

```

.SBTTL TRAP TABLE

```

:THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
:BY THE "TRAP" INSTRUCTION.

```

```

: ROUTINE
:-----

```

```

$TRPAD: $TYPE    ;; CALL=TYPE      TRAP+0(104400)  TTY TYPEOUT ROUTINE
        $TYPOC   ;; CALL=TYPOC    TRAP+1(104401)  TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $TYPOS   ;; CALL=TYPOS    TRAP+2(104402)  TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $TYPON   ;; CALL=TYPON    TRAP+3(104403)  TYPE OCTAL NUMBER (AS PER LAST CALL)
        $TYPBN   ;; CALL=TYPBN    TRAP+4(104404)  TYPE BINARY (ASCII) NUMBER

        $RDCHR   ;; CALL=RDCHR    TRAP+5(104405)  TTY TYPEIN CHARACTER ROUTINE
        $RDLIN   ;; CALL=RDLIN    TRAP+6(104406)  TTY TYPEIN STRING ROUTINE
        $RD OCT  ;; CALL=RD OCT   TRAP+7(104407)  READ AN OCTAL NUMBER FROM TTY
        TEST     ;; CALL=CHECK    TRAP+10(104410)
        TESTIT   ;; CALL=CHKIT   TRAP+11(104411)
        DECTYP   ;; CALL=TYPOC    TRAP+12(104412)

```

3086			.EVEN		
3087	017134	000310	DIST: :BLKW	200	:STATE-WIDTH DISTRIBUTION
3088	017754	010000	BUFFER: :BLKW	40%	:BUFFER AREA
3089					
3090		000001	.END		

,

CO7

QUEST	012027	825	931	2246#														
RREG	001660	829	831#															
ROCHR =	104405	2585	3080#															
RJLIN =	104406	909	1474	1463	1497	2626	3081#											
RDOCT =	104407	818	1470	3082#														
READ	007500	1833#	1871															
RELACC	010224	1909	1925#															
REST1	012216	875	893#															
RESVEC=	000010	530#																
RET	007070	1718	1726#															
RETERR	004124	1244	1246#															
RETURN	001620	821#	1535	1698	1744	1791	2071											
RYS	001404	769#	1374#	1378#	1381#	2034	2036#	2040	2053									
RMA	001376	766#	898#	1766														
RNA	001400	767#	899#	1767														
RAC	001402	768#	900#	1768														
RST	011300	799	805	2123#														
RO	=X000000	451#	792	793#	794#	795	801	807	820#	880	883	886	888	895#				
		896#	897#	914#	915#	916	919	922	925	928	933#	936#	940#	941#				
		1093#	1096#	1101#	1123#	1162#	1192#	1213#	1214#	1219#	1234#	1475#	1476	1478				
		1525#	1526#	1530	1536#	1537#	1538	1542#	1545	1548	1627#	1631	1633#	1634				
		1635	1638	1639	1689#	1690#	1691#	1692	1693#	1694#	1697#	1702#	1739#	1755#				
		1758	1769#	1771#	1774#	1782#	1785#	1786#	1787#	1788#	1789	1790#	1829#	1831#				
		1870#	1893#	1903	1913#	1915	1928#	1929	1932#	1939	1942	1970#	1972	1978#				
		1981#	1986	1989	2005#	2008#	2014	2015	2024#	2026#	2062#	2063#	2064	2065#				
		2067	2068#	2069#	2072#	2076#	2089#	2090#	2092#	2093	2103	2225#	2228	2623				
		2627#	2630	2646#	2774	2775#	2776#	2783#	2784#	2785#	2786#	2787#	2788	2793				
		2798#	2800#	2804	2806	2833	2834#	2839	2844	2847#	2839	2907#	2911	2912				
		2914#	2915#	2916	2938#	3057	3058#	3059	3060#	3061#	3062#	3063#						
R1	=X000001	452#	1553#	1554#	1628#	1629	1631#	1632#	1633	1638	1639	1742#	1756#	1770#				
		1772#	1775#	1783#	1792#	1820#	1824#	1832#	1833	1859	1914#	1919#	1925#	1931#				
		1932	1933	1971#	1979#	1984#	2001#	2013#	2017#	2025#	2028#	2083#	2090	2624				
		2628#	2632#	2634#	2636#	2639#	2642	2645#	2900	2937#	3034	3035#	3038#	3044#				
R2	=X000002	453#	1343#	1353#	1406#	1410#	1411	1416#	1418	1441#	1539#	1556#	1678#	1682#				
		1766#	1793#	1794#	1795#	1796	1799	1802	1803#	1805#	1809#	1811	1813#	1814#				
		1816#	1818	1823#	1833#	1834#	1835#	1836#	1837#	1838	1842#	1843#	1844#	1845				
		1848	1852	1873#	1881#	1884#	1885#	1898#	1896#	1897#	1898#	1899	1915#	1917#				
		1929#	1930#	1931	1948#	1972#	1973#	1974#	1975#	1976#	1977#	1978	1985#	1996#				
		2014#	2040#	2043#	2131	2133#	2134	2136#	2137	2138#	2139#	2142	2151	2159#				
		2160	2162#	2166	2168#	2625	2629#	2633#	2635#	2637#	2643	2644#						
R3	=X000003	454#	1340#	1350	1352#	1355	1447#	1449#	1487#	1502#	1711#	1712	1714#	1721				
		1776#	1821#	1859#	1860#	1861#	1862	1926#	1936	1938#	1944#	1945#	1946#	1947#				
		1948	1963	1986#	1987	1988	1989#	1990	1992	1993	1995	2137#	2143#	2144#				
		2145#	2146#	2147#	2148#	2149#	2152	2581	2582#	2583	2586#	2587	2591	2593				
		2595#	2597#	2981	2990#	2996#	2997#	3000#	3005#	3006#	3007	3016#						
R4	=X000004	455#	1308#	1312	1341#	1347	1349#	1360	1411#	1667	1747#	1748#	1749#	1750#				
		1751	1767#	1793	1796#	1797#	1798#	1800	1933#	1935#	1936	1938	1982#	1998#				
		2000	2982	2984#	2995#	2986#	2987	2988#	3002	3004#	3012#	3015#						
R5	=X000005	456#	1256#	1258#	1267#	1269#	1279#	1281#	1291#	1293#	1306#	1310#	1313#	1327#				
		1356#	1361#	1376#	1379#	1382#	1385#	1432#	1436#	1462#	1504#	1508#	1660#	1676#				
		1680#	1732	1763#	1768#	1794	1797	1799#	1800#	1801#	1939#	1940#	1941#	1951				
		1957#	1958	1983#	1999#	2000	2062	2082#	2085	2086	2095	2096#	2983	2989#				
		2991#	2993#	2994#	2995#	2996	3014#											
R6	=X000006	457#	459	834#	835#	836												
R7	=X000007	458#	460															
SWRSLB	007074	1376	1379	1382	1385	1432	1436	1504	1508	1676	1680	1732#						

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DVAAA.CMB CROSS REFERENCE TABLE -- USER SYMBOLS

TYPBN = 104404	2224	3077#												
TYPDC = 104412	1444	1641	1868	1874	1882	1896	1889	1900	1949	2041	2044	3085#		
TYPE = 104400	797	803	811	817	825	878	842	908	931	973	1325	1332	1334	
	1370	1401	1443	1445	1450	1452	1469	1473	1480	1482	1485	1486	1495	
	1496	1499	1500	1501	1521	1529	1547	1558	1573	1578	1642	1648	1650	
	1656	1669	1671	1720	1765	1858	1867	1869	1875	1878	1880	1883	1887	
	1890	1893	1895	1902	1905	1907	1911	1912	1921	1927	1950	1956	1965	
	1967	2003	2004	2007	2010	2030	2033	2042	2045	2047	2057	2059	2102	
	2108	2120	2158	2182	2219	2589	2592	2596	2733	2741	2773	2790	2792	
	2795	2797	2801	2808	2855	3008	3041	3073#						
TYPEDG 007030	1649	1711#	2046											
TYPOC = 104401	2781	2805	3074#											
TYPON = 104403	3076#													
TYPOS = 104402	814	970	1532	1550	1575	1645	1653	1664	1714	1723	1864	1953	1960	
	2050	2105	2111	3075#										
TYPOUT 011540	2167	2179#												
TYPRP 010666	1289	2033#												
TYPSET 006522	1413	1641#												
UNEXP 001442	785#	1003	1178											
VADR 001336	750#	964	1597	1598	1599									
VECTOR 001324	745#	1003#	1167#	1178#	1535#	1600#	1614#	1615#	1618	1620	1622	1698#	1744#	
	1791#	2071#												
VECTR1 001330	747#	1168#	1601#	1604#	1618#	1619#	1624#							
VECTR2 001332	748#	1194#	1602#	1620#	1621#									
VECTR3 001334	749#	1603#	1622#	1623#										
VLIN 011612	1963	2194#												
VNP 011606	2055	2192#												
VNR 011604	2053	2191#												
VSET 011610	1667	2193#												
VTFLG 002432	879	832	885	933#										
VTINIT 014054	2030	2461#												
VTSS 002206	887	890#												
VACT 001340	751#	1600	1601	1602	1603									
VE 011570	1358	1363	2184#											
V12 011576	1260	1295	2187#											
V2 011572	1315	2185#												
V26 011502	1271	1283	2189#											
V 011574	1464	2186#												
VE30 011600	1329	2188#												
TEST 001426	778#	828#	830#	906	951									
W1DE 001346	754#	1778#	1854#	1884	1897									
W1MSG 012645	1837	2331#												
W2P 004126	1248#	1579	1588											
X1DJ 012337	1485	2291#												
Y1DJ 012531	1499	2315#												
SAPTH0 001000	593	599#												
SASTAT= ***** U	2925	2940												
SATYC 016340	2896	2898#												
SATY1 016314	2394#													
SATY3 016332	2840	2095#												
SATY4 016332	2746	2037#												
SALTOB 001134	630#													
S1SE 001250	694#	954	1609	1610	1611									
S1AOR 001122	625#													
S1JOAT 001126	627#	954#	958	964#	1075#	1136#	1150#	1151	1172#	1196#	1228#	1229#	1230	
	1242#	1243	2087#	2088	2529	2531	2534							

\$MMS1	001224	675#															
\$MMS2	001230	683#															
\$MMS3	001234	686#															
\$MMS4	001240	689#															
\$MOR	001002	601#															
\$MFLG	016556	2895#	2901	2936*	2940#												
\$MNEW	015103	2609#															
\$MSGAD	001210	661#	2911#	2914													
\$MSGLG	001212	662#	2916#														
\$SGTY	001174	655#	2909	2917*	2929	2933*											
\$SWR	015072	2607#															
\$MTYP1	001225	676#															
\$MTYP2	001231	684#															
\$MTYP3	001235	687#															
\$MTYP4	001241	690#															
\$XCNT	015472	2701	2711#														
\$NULL	001154	639#	2852	2891													
\$MWTST=	000001	980#	992#	999#	1008#	1016#	1023#	1030#	1037#	1048#	1059#	1066#	1079#	1089#			
		1102#	1116#	1126#	1140#	1154#	1184#	1204#	1220#	1249#	1262#	1274#	1285#	1300#			
		1317#	1335#	1365#	1396#	1421#											
\$OCNT	017004	2980#	3009#	3022#													
\$OMODE	017006	2975#	2979#	2984	2987*	2998*	3024#										
\$OVER	015456	2665	2681	2689	2699	2708#											
\$PRSS	001202	658#	868*	944*	1426	2098	2116	2211*	2212*	2235	2695	2712					
\$PASTM	001006	603#															
\$QUES	001170	646#	1480	2589	2605	2765	2891										
\$ROCHR	014622	2553#	3080														
\$RODEC=	##### U	3083															
\$-DLIN	014742	2581#	3081														
\$ROOCT	015114	2621#	3082														
\$ROSZ =	000010	2574#															
\$RTNAD	011756	2234#															
\$RZA =	##### U	3033															
\$SAVRE =	##### U	3083															
\$SCOPE	015216	840	2663#														
\$SETUP=	000027	714#	839	840	842	844	846	847	848	850	2209	2542	2611	2664			
		2727	2753	2760													
\$STUP =	177777	714#															
\$SVLAD	015422	2673	2702#														
\$SVPC =	000214	577#	582														
\$SWR =	167400	421#	431	545	546	547	548	549	550	551	552	643	644	645			
		847	848	850	851	984	996	1003	1012	1020	1027	1034	1041	1052			
		1063	1070	1083	1093	1106	1120	1130	1144	1158	1188	1208	1224	1253			
		1266	1278	1289	1304	1321	1339	1369	1400	1425	2204	2210	2227	2233			
		2235	2655	2656	2657	2658	2659	2664	2676	2678	2679	2682	2683	2684			
		2691	2692	2693	2705	2708	2711	2718	2719	2720	2721	2722	2731	2738			
		2750	2753	2765													
\$SWPEG	001216	666#	871														
\$-MK=	000000	552	553	2659	2660	2680											
\$TESTN	001200	657#	2703#														
\$TIMES	001160	643#	847#	1070*	1083*	1106#	1253*	1266#	1278*	1289*	1304*	1321*	1339*	1369*			
		1400#	1425#	2210#	2691#	2698	2701#	2711									
\$TKB	001146	636#	793	940	1460	2540	2557	2563									
\$TKS	001144	635#	877#	910#	934	1074#	1086#	1114#	1454*	1455	1461*	2124*	2197*	2540			
		2555	2561														
\$TN =	000040	421#	431	980	984#	992	996#	999	1003#	1008	1012#	1016	1020#	1023			

K07

.KT11	10		
.SETUP	10	4210	714
.SURHI	10	4210	541
.SURL0	5530		
.SACT1	10	4210	573
.SAPT8	10	4210	6500
.SAPTH	10	4210	584
.SAPTY	10	4210	2891
.SASTA	10		
.SCATC	10	4210	560
.SCHTA	10	4210	606
.SDB20	10		
.SDB20	10		
.SDIV	10		
.SEOP	10	4210	2200
.SERRO	10	4210	2712
.SERRT	10	4210	2765
.SMULT	10		
.SPARM	4210		
.SPOWE	10	4210	
.SRAND	10	4210	
.SRODE	10		
.S700C	10	4210	2611
.S AD	10	4210	2537
.S RZ	10		
.S WVE	10	4210	
.S730	10		
.SS020	10		
.SSCOP	10	4210	2649
.SSIZE	10		
.SSPAC	4210		
.SSUPR	10		
.SSWDO	4210		
.STRAP	10	4210	3049
.STYP8	10	4210	3025
.STYPD	10	4210	
.STYPE	10	4210	2912
.STYPO	10	4210	2948
.S40CA	10		
.1170	10		

ADC	1707	1795	1798	1801	1837	1947	1976	2081							
ADCB	3040														
ADG	823	964	1245	1378	1384	1438	1466	1467	1510	1597	1598	1599	1600	1601	1602
	1603	1607	1613	1619	1621	1623	1678	1701	1719	1740	1786	1793	1794	1796	1797
	1799	1800	1885	1897	1898	1931	1977	2063	2075	2639	2787	2848	2908	2920	2932
	2976	2986													
ASL	961	988	1596	1733	1734	1735	1736	1813	1842	2632	2634	2636	2784	2785	2786
	3061														
ASR	1704	1705	1706	1761	1834	1835	1836	1844	1861	1941	1944	1945	1946	1973	1974
	1975	2078	2079	2080	2143	2144	2145	2146	2147	2915					
BEQ	810	870	887	904	952	963	1045	1076	1137	1152	1231	1415	1427	1477	1479
	1544	1595	1630	1718	1804	1810	1812	1819	1877	1892	1909	1969	2021	2167	2226
	2631	2679	2681	2683	2687	2696	2729	2732	2754	2757	2789	2794	2807	2838	2851
	2896	2902	2906	2926	2928	3003	3039								
BGE	1351	1754	1846	1991	2699										
BGT	1668	1759	2054	2056	2094	2215	2571	3010							
BHI	2635														
BIC	794	877	941	1098	1229	1526	1615	1803	2138	2148	2212	2558	2564	2572	2638
	2727	3000													
BICB	915														
BIS	910	960	1074	1086	1114	1537	1691	1745	1750	1788	2124	2134	2149	2197	3005
	3006														
BISB	2073	2179	2180	2181	2776										
BIT	1044	1055	1527	1543	2020	2664	2678	2686	2693	2731	2738	2753			
BITB	869	2837	2842	2874	2905										
BLE	1348	1853	1904	1937	1964	1994	2161								
BLOS	2584														
BLT	1839	2135	2569	2865	3011										
E4I	935	949	1442	1456	1822										
BNE	796	802	808	822	837	859	875	881	884	889	907	917	920	923	926
	929	937	990	1056	1097	1215	1244	1299	1354	1392	1395	1448	1458	1528	1555
	1557	1636	1695	1703	1757	1762	1773	1784	1806	1825	1830	1849	1856	1871	1920
	1943	1980	1997	2002	2018	2027	2029	2070	2077	2099	2117	2171	2175	2560	2566
	2538	2594	2665	2694	2739	2744	2761	2777	2799	2836	2843	2845	2853	2861	2875
	2832	2904	2910	2913	2930	3001									
BPL	1043	1054	1110	1122	1135	1149	1171	1210	1346	1409	1815	1934	2035	2038	2051
	2132	2141	2151	2157	2556	2562	2751	2830	2879	2999					
BR	826	829	861	932	939	955	1176	1200	1330	1331	1333	1419	1451	1459	1465
	1481	1491	1492	1514	1515	1546	1559	1565	1581	1590	1606	1640	1817	1826	1829
	1841	1851	1879	1894	1906	1966	2023	2178	2180	2567	2590	2640	2667	2673	2676
	2689	2692	2749	2782	2809	2832	2858	2868	2877	2884	2896	2918	2977	2992	3013
	3043														
CLC	3042														
CLR	789	828	835	847	848	858	873	911	933	944	945	957	1071	1107	1130
	1131	1132	1146	1164	1179	1203	1235	1305	1341	1371	1374	1375	1406	1453	1454
	1520	1522	1604	1624	1632	1696	1738	1739	1771	1776	1777	1778	1779	1780	1781
	1782	1827	1917	1925	1926	2024	2036	2039	2066	2125	2133	2209	2210	2628	2629
	2691	2706	2775	2990											
CLRB	1617	2163	2164	2165	2172	2176	2595	2690	2857	2883	2934	2935	2936		
CMP	809	821	836	858	880	883	896	898	966	989	1151	1177	1201	1230	1243
	1298	1347	1350	1391	1394	1414	1629	1635	1638	1639	1667	1717	1753	1758	1811
	1818	1838	1845	1852	1903	1936	1942	1963	1990	1993	2000	2053	2055	2093	2134
	2160	2559	2565	2568	2570	2583	2674	2698	2760						
CMPB	795	801	807	916	919	922	925	928	1476	1478	2170	2174	2587	2593	2680
	2684	2743	2835	2850	2852	2860	2881	2885	2903						
DEC	936	962	974	1096	1214	1353	1447	1554	1556	1605	1694	1702	1756	1772	1783

MO7

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 DVADAA.CMB CROSS REFERENCE TABLE -- PERMANENT SYMBOLS

	1805	1824	1829	1870	1919	1979	1996	2001	2017	2026	2028	2069	2076	2168	2213
DECB	2783														
EMT	2864	2867	2998	3009											
HALT	435														
INC	566	2022	2752	2762	2831										
	787	890	959	1094	1108	1120	1297	1390	1393	1457	1540	1612	1699	1755	1807
	1814	1820	1821	1840	1843	1847	1850	1854	1857	1957	2211	2697	2734	2933	3004
	3012														
INCB	1344	2169	2173	2177	2702	2728	2887								
IOT	436														
JMP	570	571	572	800	806	905	918	921	924	927	930	1494	1517	1567	1583
	1592	2198	2233												
JSR	799	805	879	882	885	894	1163	1175	1182	1193	1199	1202	1256	1258	1267
	1269	1279	1281	1291	1293	1305	1310	1313	1324	1326	1327	1356	1361	1373	1376
	1379	1382	1385	1389	1405	1407	1408	1413	1428	1432	1436	1462	1488	1489	1490
	1504	1508	1512	1513	1562	1563	1564	1571	1572	1579	1580	1587	1588	1589	1649
	1660	1676	1680	1910	1916	1918	1922	2006	2009	2011	2016	2046	2228	2740	2746
	2840	2859	2866	2673	2922										
MOV	786	792	793	798	804	812	819	820	830	834	838	840	841	842	843
	844	845	846	850	851	854	855	856	857	862	864	865	866	871	895
	896	897	898	899	900	901	912	914	940	946	947	950	953	954	955
	956	968	975	976	977	983	984	985	996	1003	1004	1012	1020	1027	1034
	1041	1052	1063	1070	1072	1075	1083	1084	1093	1095	1101	1106	1123	1133	1136
	1144	1145	1147	1150	1162	1165	1167	1168	1169	1172	1173	1178	1180	1192	1194
	1195	1196	1197	1208	1211	1212	1213	1219	1224	1225	1228	1234	1241	1242	1252
	1253	1254	1255	1266	1278	1289	1290	1304	1308	1309	1321	1322	1323	1339	1340
	1342	1343	1349	1352	1355	1360	1369	1372	1388	1400	1402	1403	1404	1411	1412
	1416	1417	1418	1425	1431	1434	1435	1441	1449	1461	1471	1472	1475	1487	1502
	1503	1506	1507	1519	1523	1525	1530	1535	1538	1539	1542	1545	1548	1553	1561
	1566	1570	1574	1582	1586	1591	1608	1609	1610	1611	1614	1618	1620	1622	1625
	1627	1628	1631	1633	1643	1651	1657	1659	1662	1675	1679	1698	1692	1693	1697
	1698	1711	1712	1721	1732	1737	1741	1742	1744	1747	1748	1751	1766	1767	1768
	1769	1770	1774	1775	1785	1789	1790	1791	1792	1802	1809	1816	1823	1831	1832
	1833	1859	1862	1873	1881	1894	1888	1896	1899	1913	1914	1915	1928	1929	1932
	1933	1938	1939	1948	1951	1958	1970	1971	1972	1978	1981	1982	1983	1984	1985
	1986	1987	1988	1989	1992	1995	1998	1999	2005	2008	2013	2014	2025	2040	2043
	2048	2062	2064	2067	2068	2071	2072	2085	2086	2087	2089	2089	2100	2101	2103
	2109	2118	2119	2126	2136	2137	2162	2216	2223	2225	2553	2554	2581	2582	2597
	2598	2599	2600	2621	2622	2623	2624	2625	2627	2642	2643	2644	2645	2646	2669
	2670	2672	2675	2678	2700	2701	2704	2705	2708	2709	2730	2735	2755	2758	2774
	2779	2788	2793	2798	2800	2804	2833	2834	2839	2847	2862	2899	2900	2907	2911
	2916	2917	2919	2921	2931	2937	2939	2973	2981	2982	2983	2989	2996	3014	3015
	3016	3017	3018	3034	3035	3044	3045	3046	3057	3058	3062				
MOV B	849	1226	1227	1616	1689	1743	1872	1901	2142	2152	2557	2563	2566	2591	2630
	2703	2707	2737	2745	2844	2872	2860	2894	2895	2897	2974	2975	2978	2979	2980
	2984	2987	2988	3007	3037	3060									
NEG	1410	1935	2092	2159	2985										
NOP	2208	2229	2230	2231											
RESET	831	893	902	1073	1095	1111	1493	1516	2123	2227					
ROL	2633	2635	2637	2991	2993	2994	2995	2997	3038						
RTI	790	824	863	913	1166	1181	1246	1524	2127	2183	2573	2601	2647	2710	2764
	2849	3019	3047												
RTS	942	978	1237	1429	1440	1446	1468	1637	1670	1672	1683	1708	1726	1763	2012
	2019	2031	2058	2060	2082	2096	2114	2121	2128	2153	2802	2889	2939	3063	
SEC	3036														
SUB	1312	1381	1387	1439	1511	1658	1682	1760	1860	1930	1940	2090	2736	2914	

SWAB	1536	1690	1749	1787	2065										
TRAP	3065	3074	3075	3076	3077	3080	3081	3082	3083	3084	3085				
TST	874	903	906	938	951	958	1183	1426	1460	1484	1498	1594	1634	1848	1855
	1876	1891	1908	1968	2015	2034	2037	2095	2098	2116	2131	2156	2166	2671	2671
	2695	2750	2756	2806	2846	2854	2876	2909	2927	2929	3002	3059			
TSTB	934	948	1042	1053	1109	1121	1134	1148	1170	1209	1345	1455	2140	2150	2555
	2561	2682	2829	2878	2901	2912	2925								
WAIT	1541	1700	1746	1752	1808	2074									
.ASCII	646	647	2291	2298	2354	2364	2377	2385	2391	2427	2465	2472	2475	2478	2481
.ASCIIZ	645	648	2222	2238	2241	2255	2257	2258	2261	2262	2263	2265	2268	2269	2271
	2274	2276	2277	2279	2283	2294	2304	2308	2315	2318	2319	2322	2325	2331	2337
	2342	2349	2351	2358	2361	2368	2372	2403	2414	2420	2424	2430	2436	2437	2438
	2439	2441	2443	2444	2484	2488	2492	2496	2500	2504	2509	2518	2605	2606	2607
	2609	2810													
.BLKB	2604														
.BLKW	3087	3088													
.BYTE	615	616	621	622	630	631	639	640	641	642	664	665	675	676	683
	684	686	687	689	690	815	816	971	972	1533	1534	1551	1552	1576	1577
	1646	1647	1654	1655	1665	1666	1715	1716	1724	1725	1865	1866	1954	1955	1961
	1962	2051	2052	2106	2107	2112	2113	2235	2245	2246	2247	2249	2251	2447	2455
	2461	2523	2524	2525	2526	2602	2603	2747	2748	2940	2941	2942	3020	3021	3022
	3023	3048													
.DSABL	2542														
.ENABL	1	421	2540												
.END	3090														
.ENDC	426	435	527	541	549	551	552	553	571	576	580	582	587	589	596
	609	613	615	643	644	645	646	650	653	675	683	686	689	692	693
	694	695	696	697	714	787	916	817	838	839	842	844	846	847	848
	850	852	873	908	918	921	924	927	930	936	939	940	950	953	964
	966	972	973	981	982	983	984	991	993	994	995	996	1000	1001	1002
	1003	1009	1010	1011	1012	1017	1018	1019	1020	1024	1025	1026	1027	1031	1032
	1033	1034	1038	1039	1040	1041	1044	1046	1049	1050	1051	1052	1055	1057	1060
	1061	1062	1063	1067	1068	1069	1070	1071	1077	1080	1081	1082	1083	1090	1091
	1092	1093	1103	1104	1105	1106	1107	1117	1118	1119	1120	1127	1128	1129	1130
	1138	1141	1142	1143	1144	1153	1155	1156	1157	1158	1177	1185	1186	1187	1189
	1205	1206	1207	1208	1221	1222	1223	1224	1232	1245	1250	1251	1252	1253	1254
	1263	1264	1265	1266	1267	1275	1276	1277	1278	1279	1285	1287	1288	1299	1290
	1300	1301	1302	1303	1304	1305	1318	1319	1320	1321	1322	1334	1336	1337	1338
	1339	1366	1367	1368	1369	1370	1393	1396	1397	1398	1399	1400	1401	1416	1420
	1422	1423	1424	1425	1425	1443	1449	1452	1457	1459	1460	1466	1478	1480	1492
	1492	1493	1515	1516	1529	1534	1535	1532	1553	1647	1648	1655	1656	1656	1667
	1716	1717	1725	1726	1826	1867	1935	1936	1962	1963	2022	2024	2022	2053	2107
	2108	2113	2114	2203	2204	2206	2209	2215	2218	2219	2222	2225	2227	2233	2235
	2236	2540	2541	2542	2546	2574	2575	2582	2584	2587	2589	2605	2611	2614	2616
	2649	2652	2655	2660	2664	2666	2677	2680	2684	2687	2689	2689	2693	2697	2702
	2704	2708	2711	2712	2715	2718	2728	2735	2740	2741	2742	2750	2760	2764	2765
	2768	2783	2812	2815	2844	2894	2895	2898	2925	2940	2951	3038	3052	3058	3061
	3073	3074	3075	3076	3077	3078	3079	3080	3081	3082	3083	3084	3085		
.EQUV	435	436	444	459	460	489	490	491	492	493	494	495		497	498
	517	518	519	520	521	522	523	524	525	526					
.EVEN	653	2222	2236	2271	2277	2811	2923	3086							
.IF	595	608	612	614	643	644	645	649	650	652	675	683	686	689	692
	693	694	695	696	697	699	714	715	817	833	839	842	846	847	848
	846	847	848	850	851	852	853	854	855	856	857	858	859	860	861
	952	963	965	971	972	980	982	984	985	986	987	988	989	990	991

.NLIST	1	421	541	552	566	643	650	653	714	852	980	984	992	996	999
	1003	1008	1012	1016	1020	1023	1027	1030	1034	1037	1041	1048	1052	1059	1063
	1066	1070	1079	1093	1089	1093	1102	1106	1116	1120	1126	1130	1140	1144	1154
	1158	1184	1188	1204	1208	1220	1224	1249	1253	1262	1266	1274	1278	1285	1289
	1300	1304	1317	1321	1335	1339	1365	1369	1396	1400	1421	1425	2209	2222	2227
	2574	2659	2760	3065	3073	3074	3075	3076	3077	3078	3080	3081	3082	3083	3084
.PAGE	3085	3086													
	606	699													
.REM	1														
.REPT	566														
.SBTTL	431	541	560	559	573	584	606	650	699	741	791	827	832	876	891
	980	992	999	1008	1016	1023	1030	1037	1048	1059	1066	1079	1089	1102	1116
	1126	1140	1154	1184	1204	1220	1247	1249	1262	1274	1285	1300	1317	1335	1365
	1396	1421	1518	1560	1569	1585	1593	2200	2237	2537	2611	2649	2712	2765	2812
	2891	2948	3025	3049	3065										
.TITLE	421														
.WORD	558	566	567	568	581	600	601	602	603	604	605	614	617	618	619
	F3	623	624	625	626	627	628	629	632	633	631	655	656	657	658
	659	660	661	662	666	667	668	681	685	688	691	692	693	694	695
	636	2214	2217	2234	2648	2791	2796	2841	2888	2923	3024				

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

* , DVADAA.SEG/SOL/CRF/PAGNUM/NL:TOC/DS:ERFZ=DVADAA.SML, DVADAA.CMB
 RUN-TIME: 39 51 7 SECONDS
 RUN-TIME RATIO: 173/98=1.7
 CORE USED: 34K (67 PAGES)

